

FIG.1

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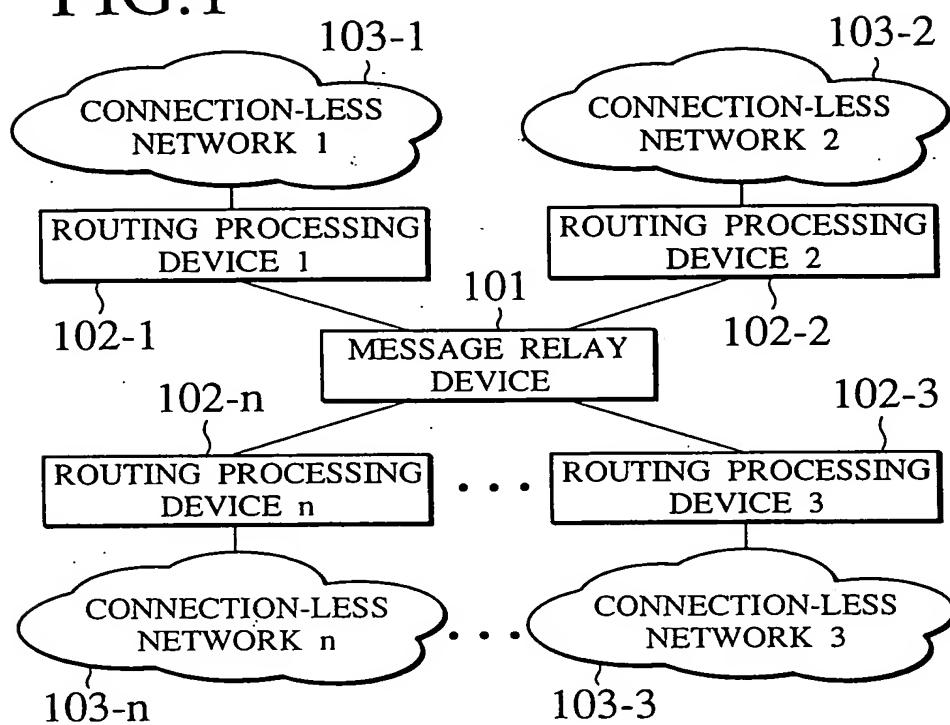


FIG.2

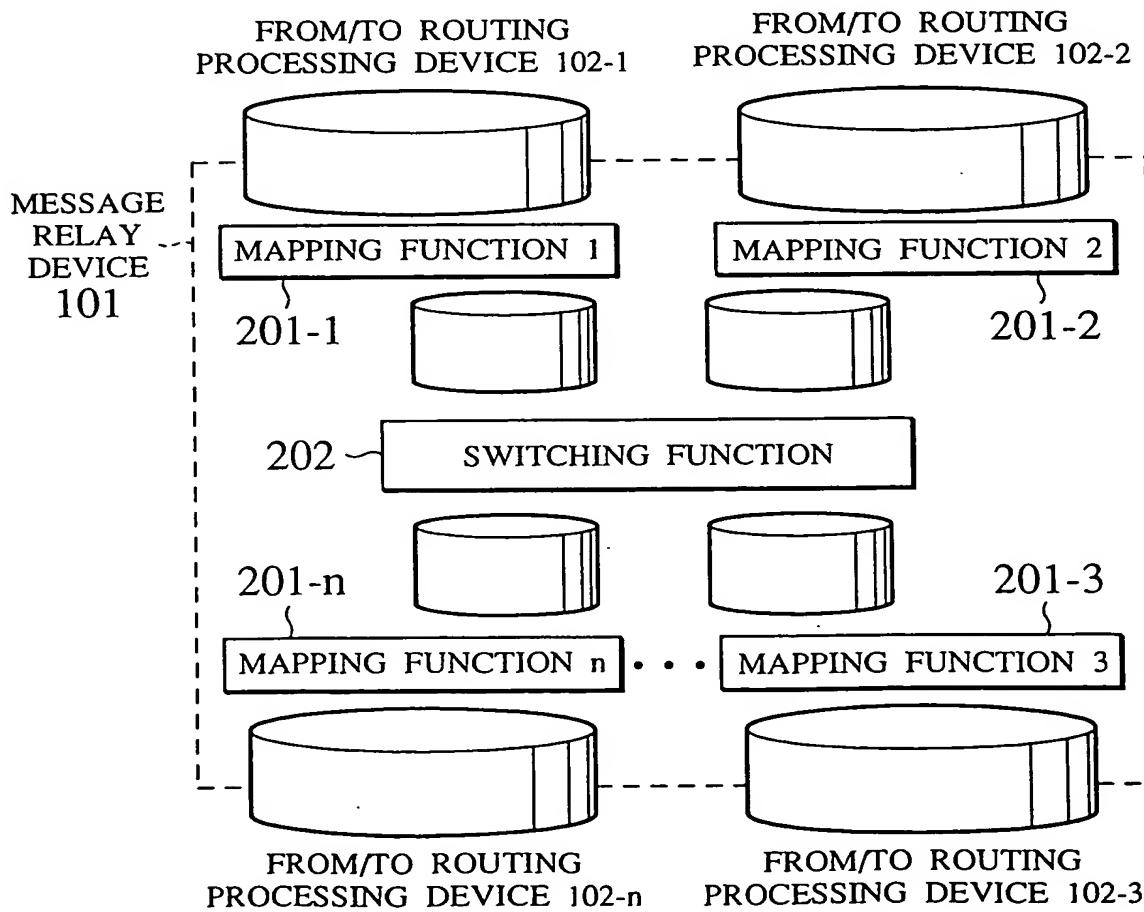


FIG.3

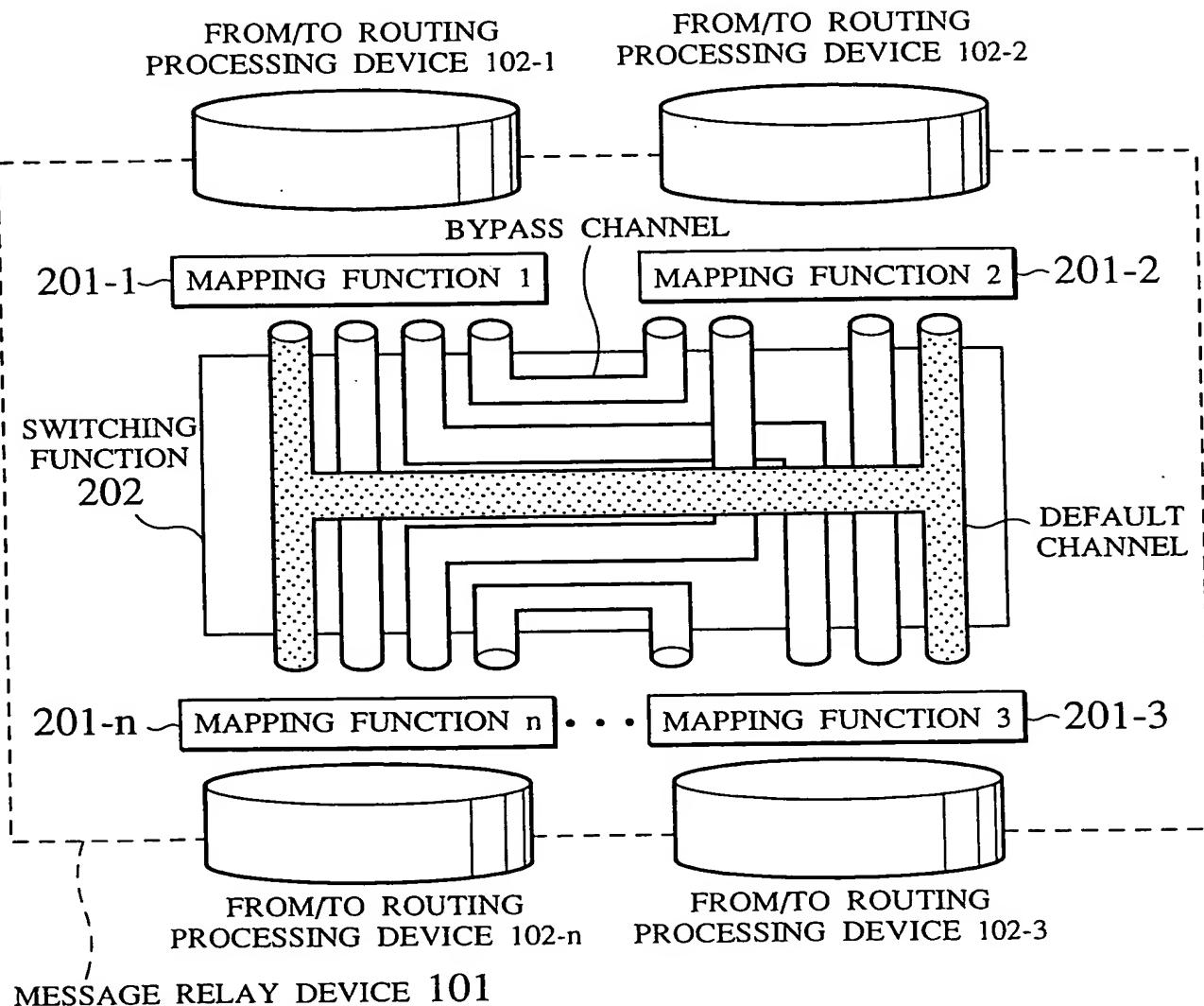


FIG.4

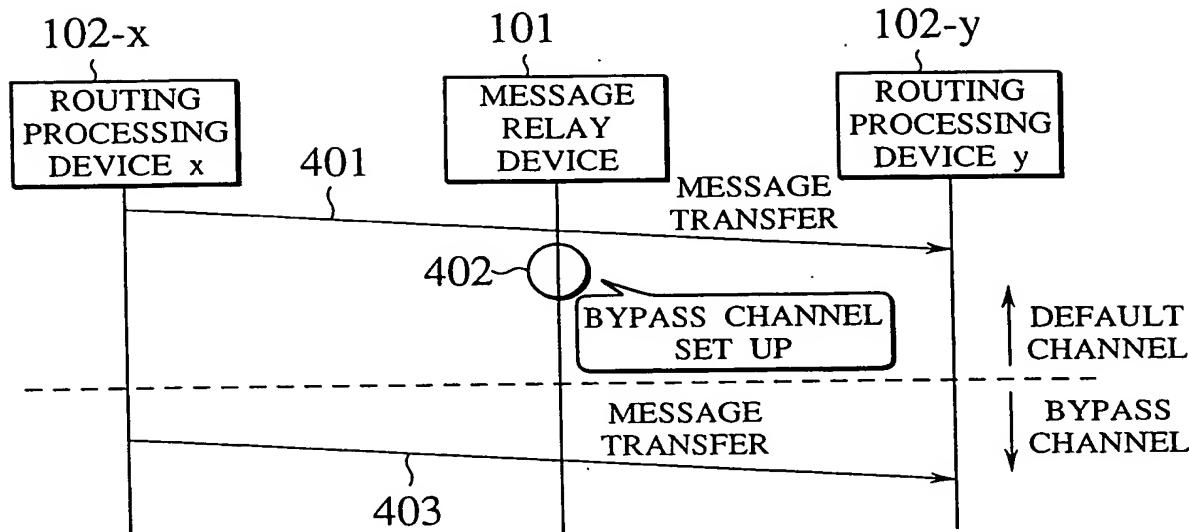
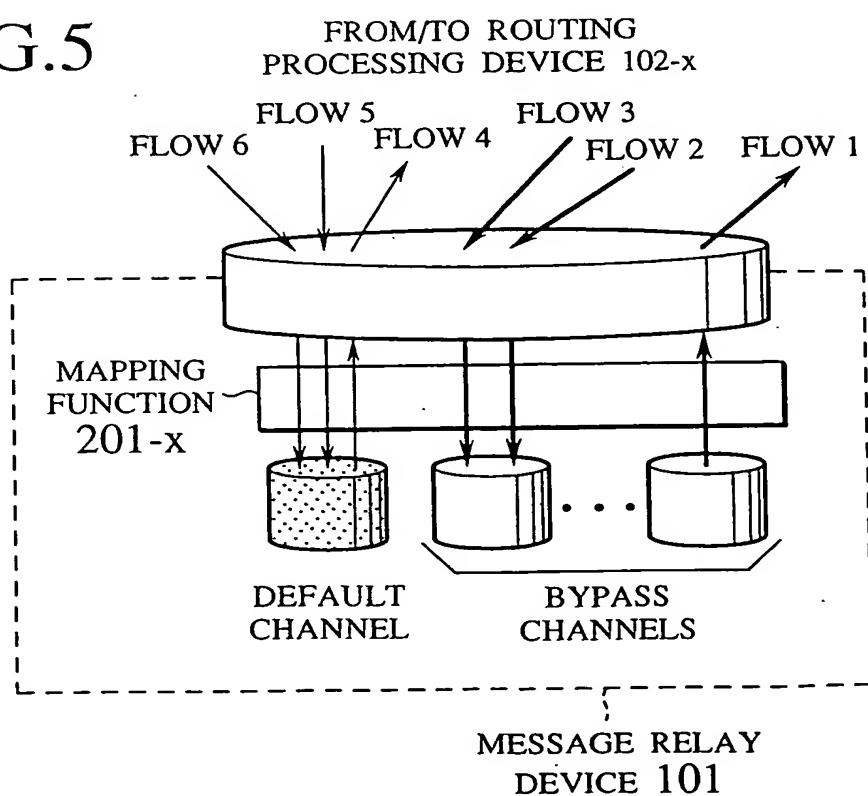


FIG.5



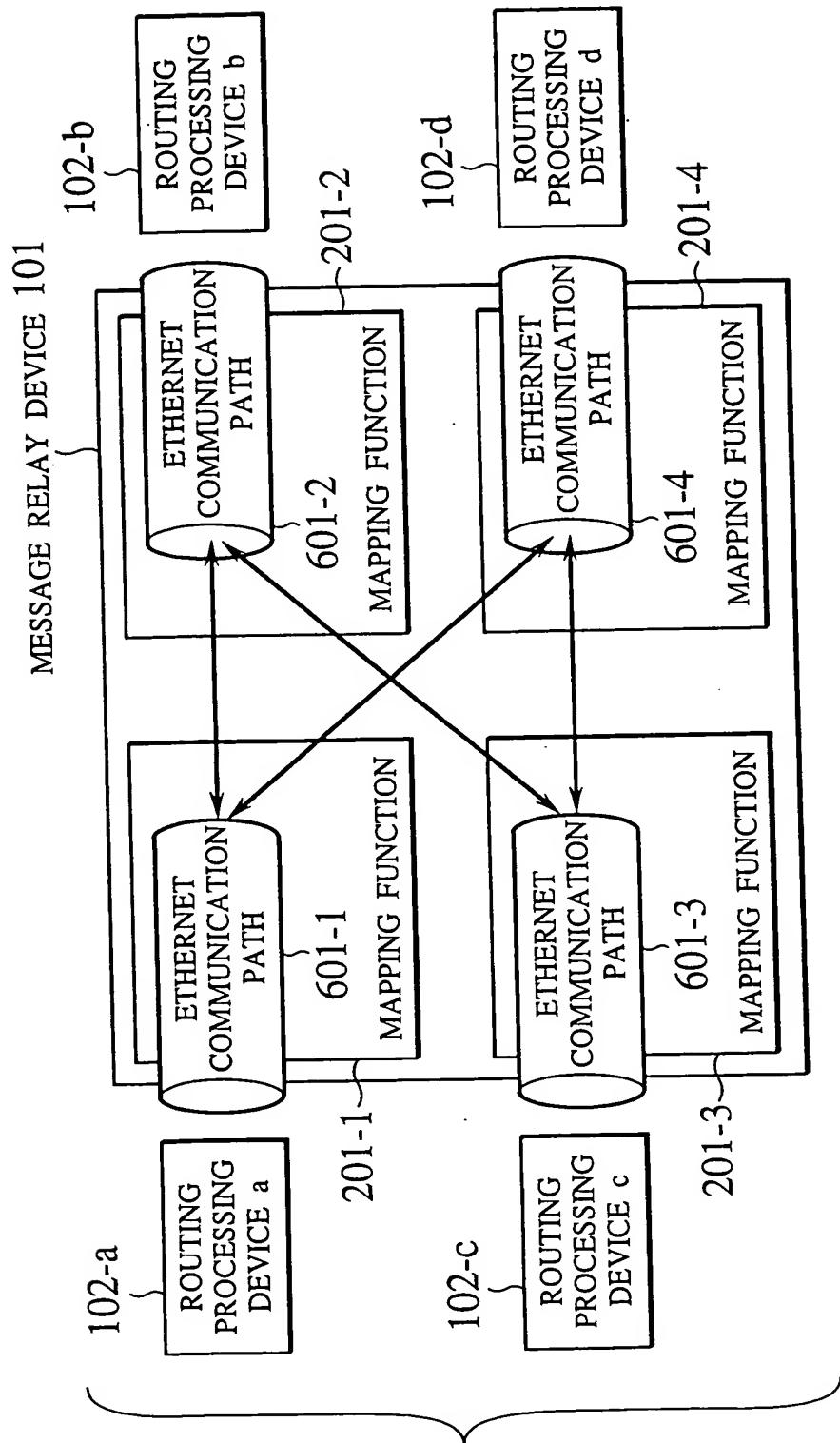
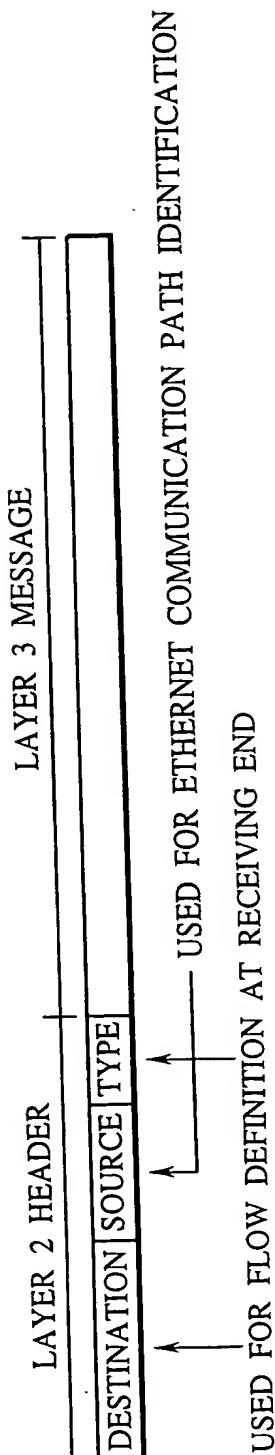
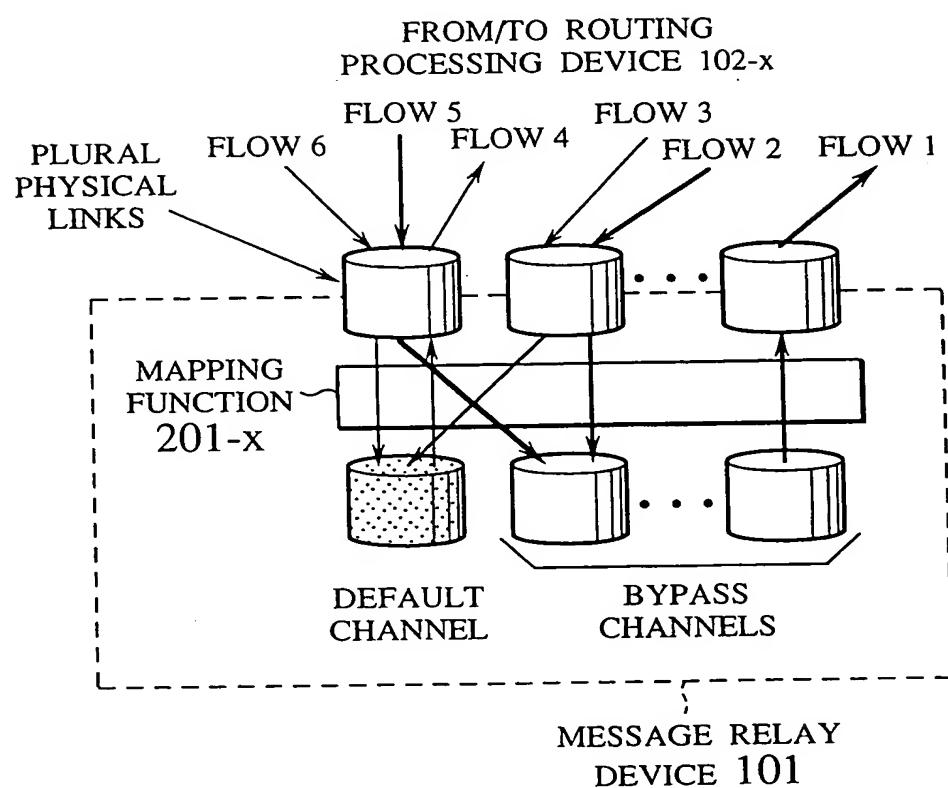


FIG. 6



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FIG.8



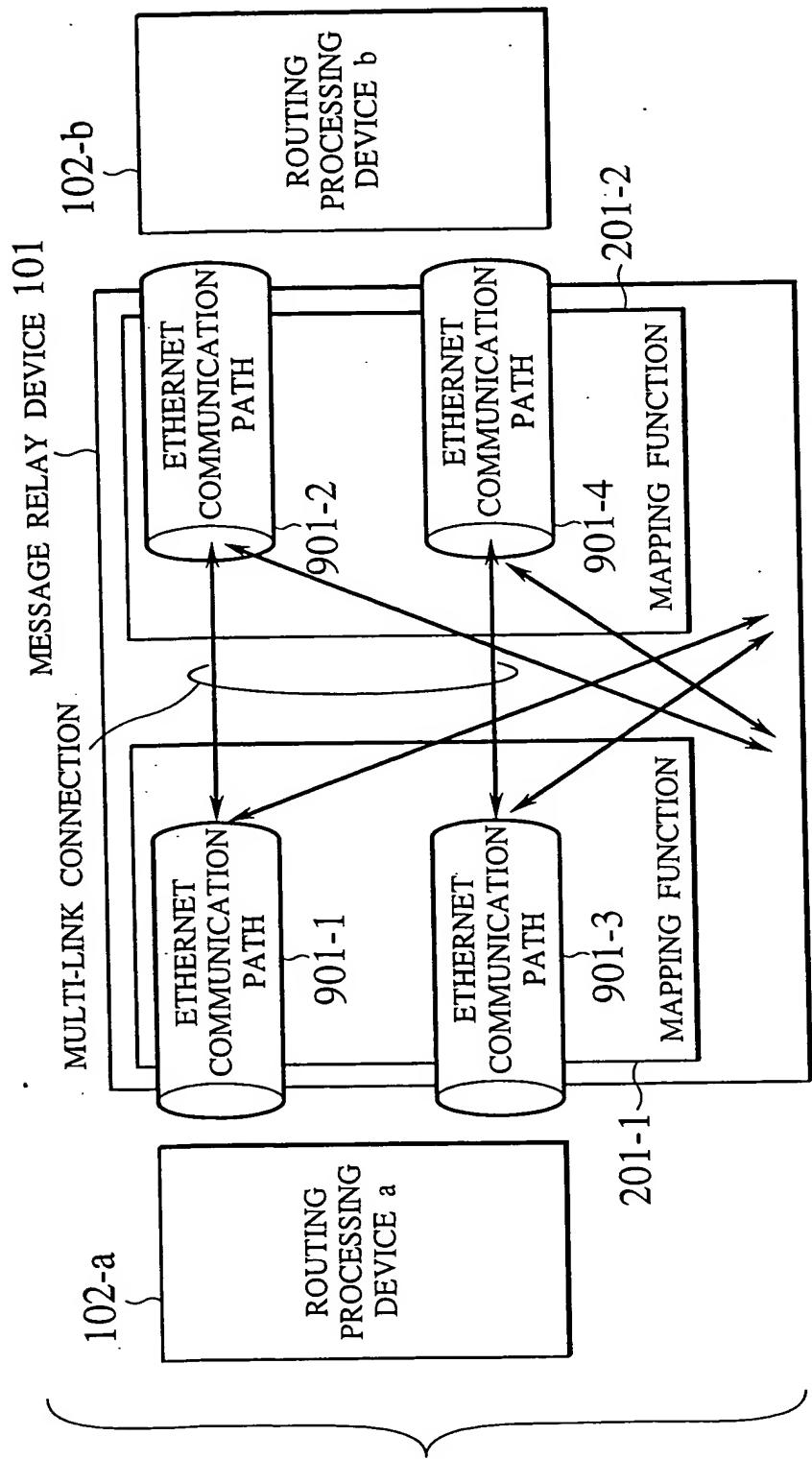


FIG. 9

FIG. 10

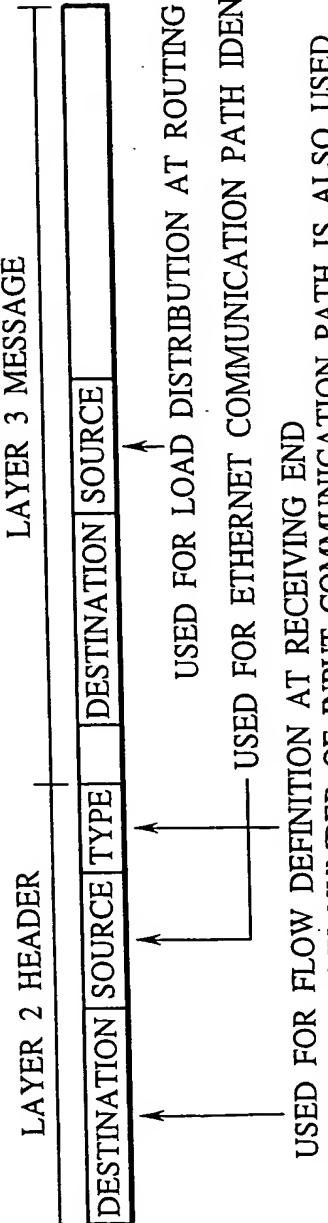
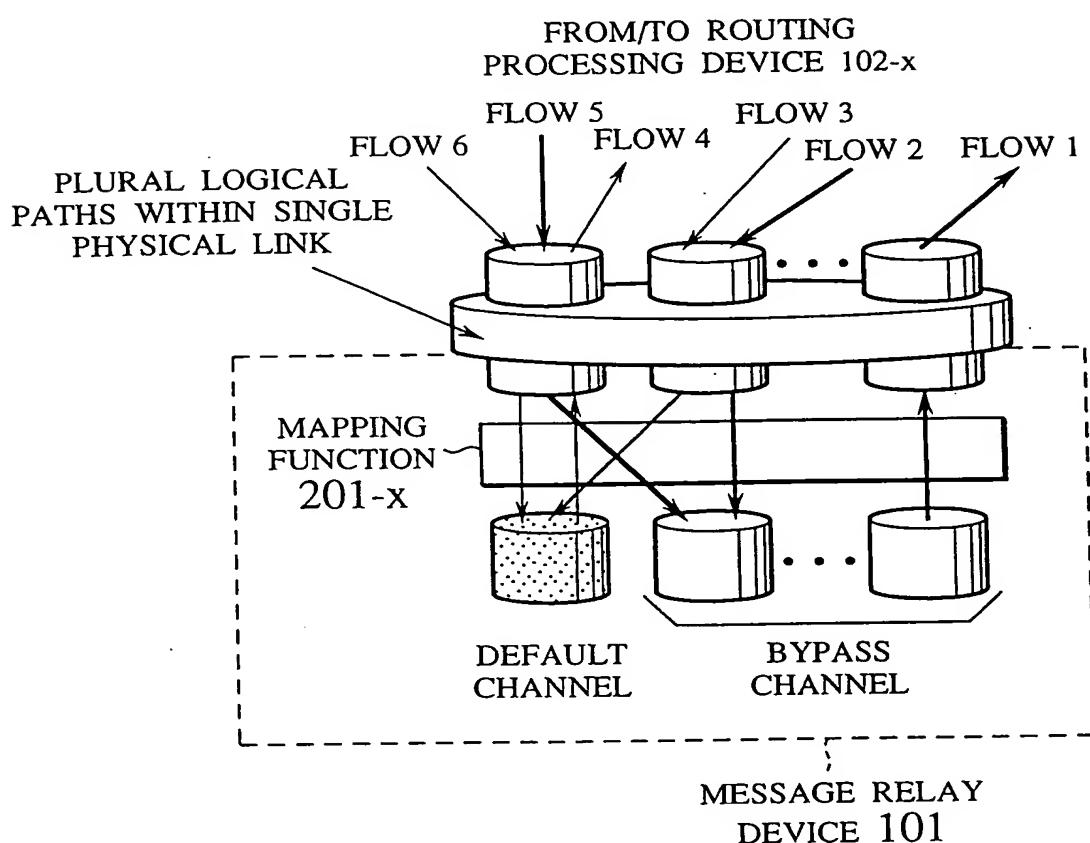


FIG. 11



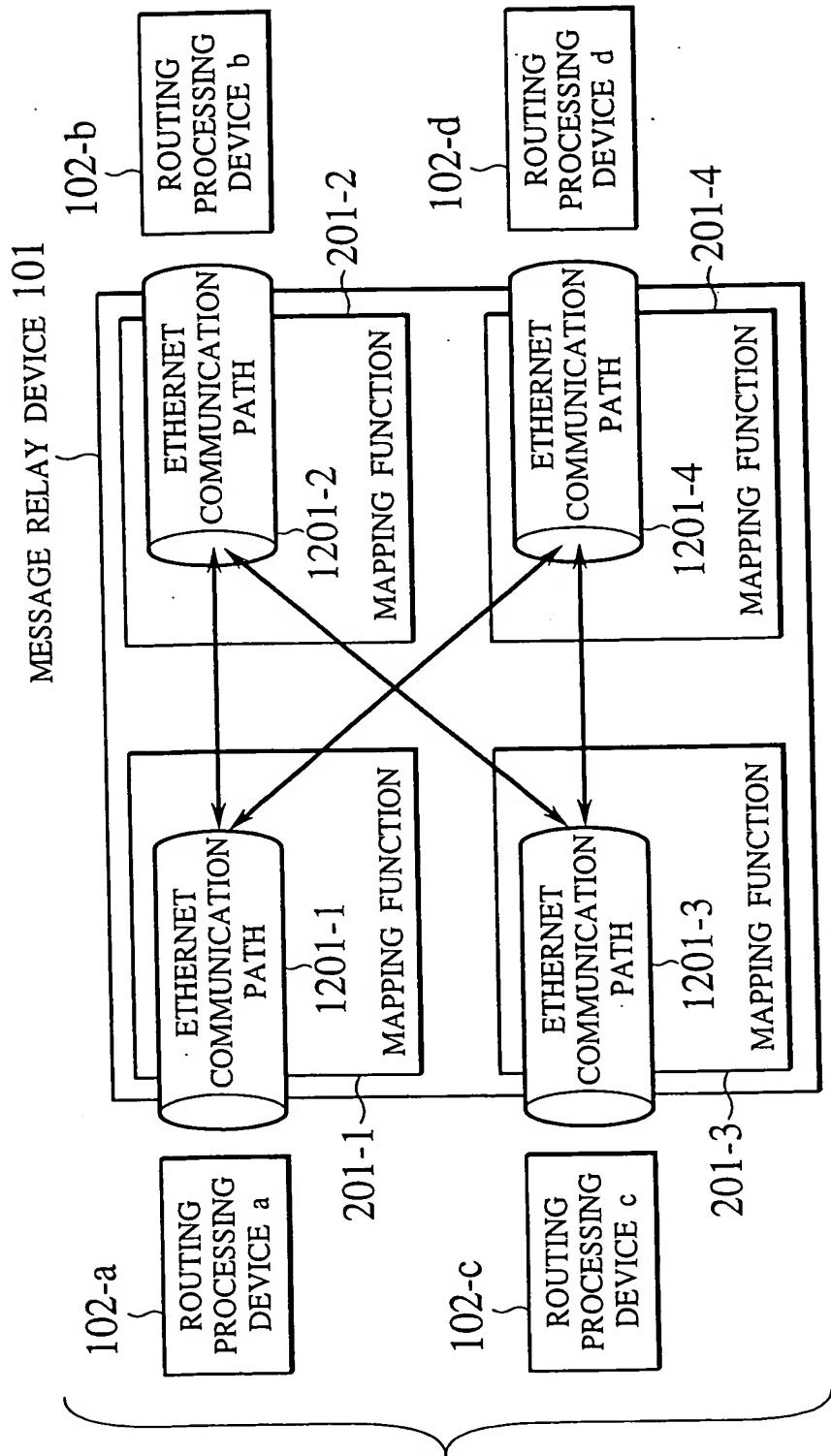


FIG. 12

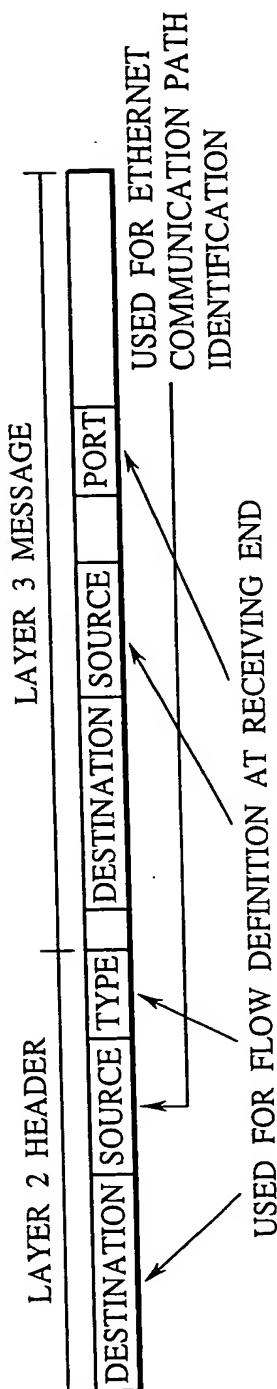
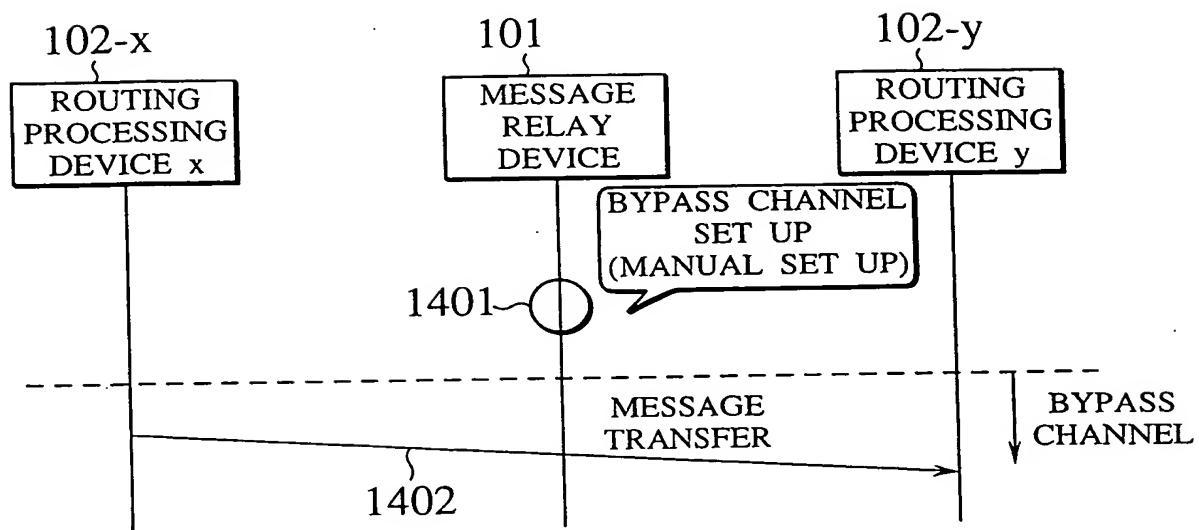
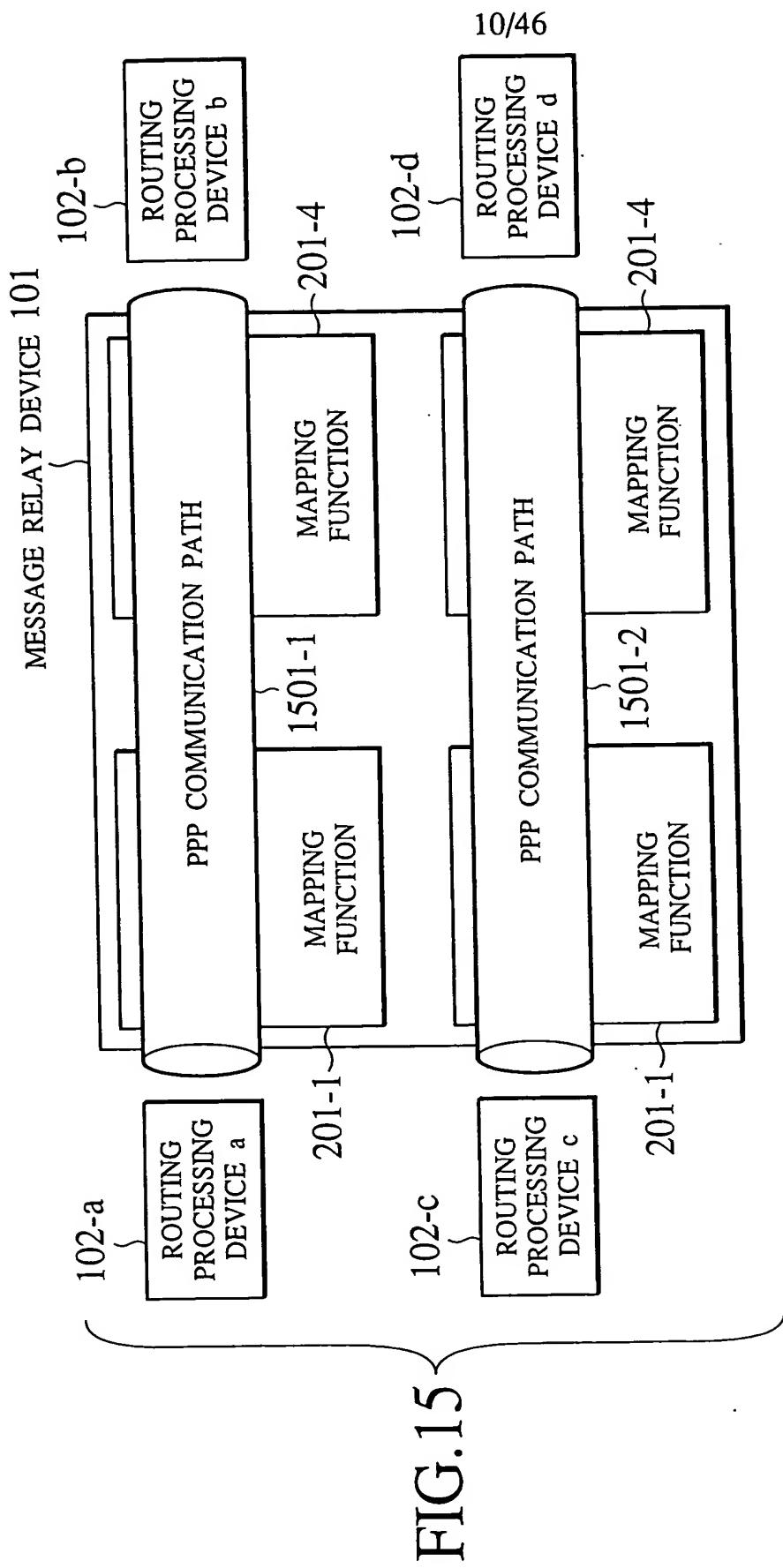


FIG. 13

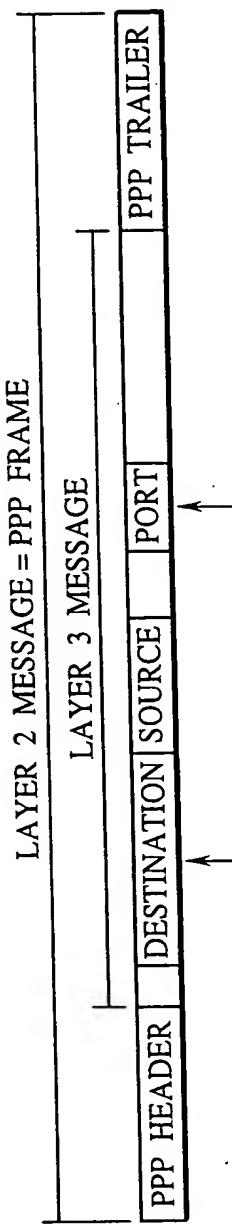
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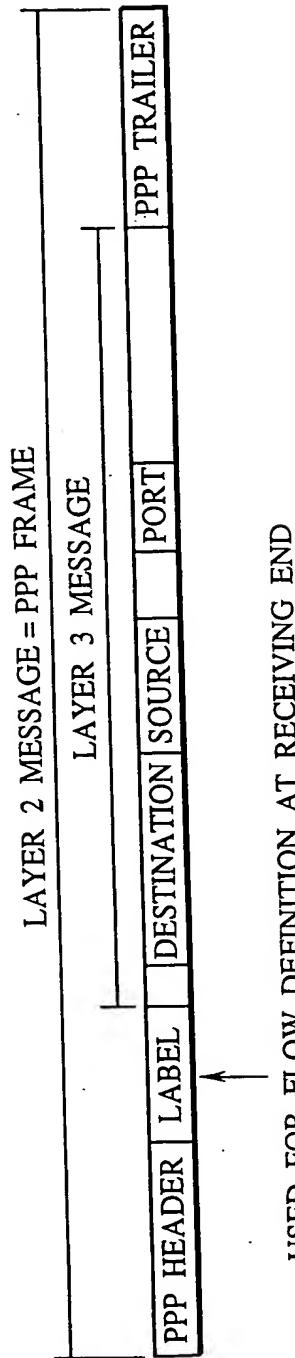
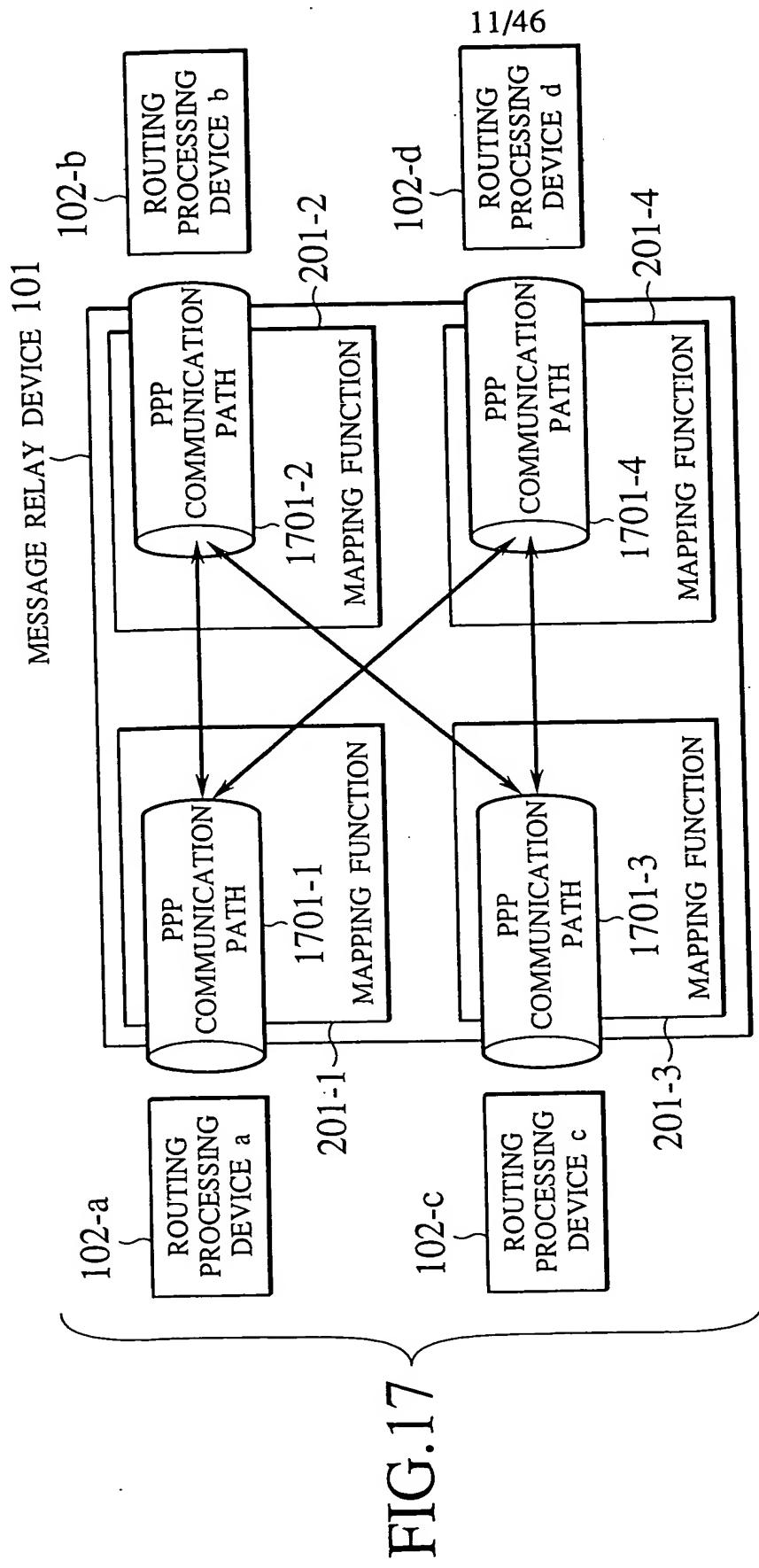
FIG.14



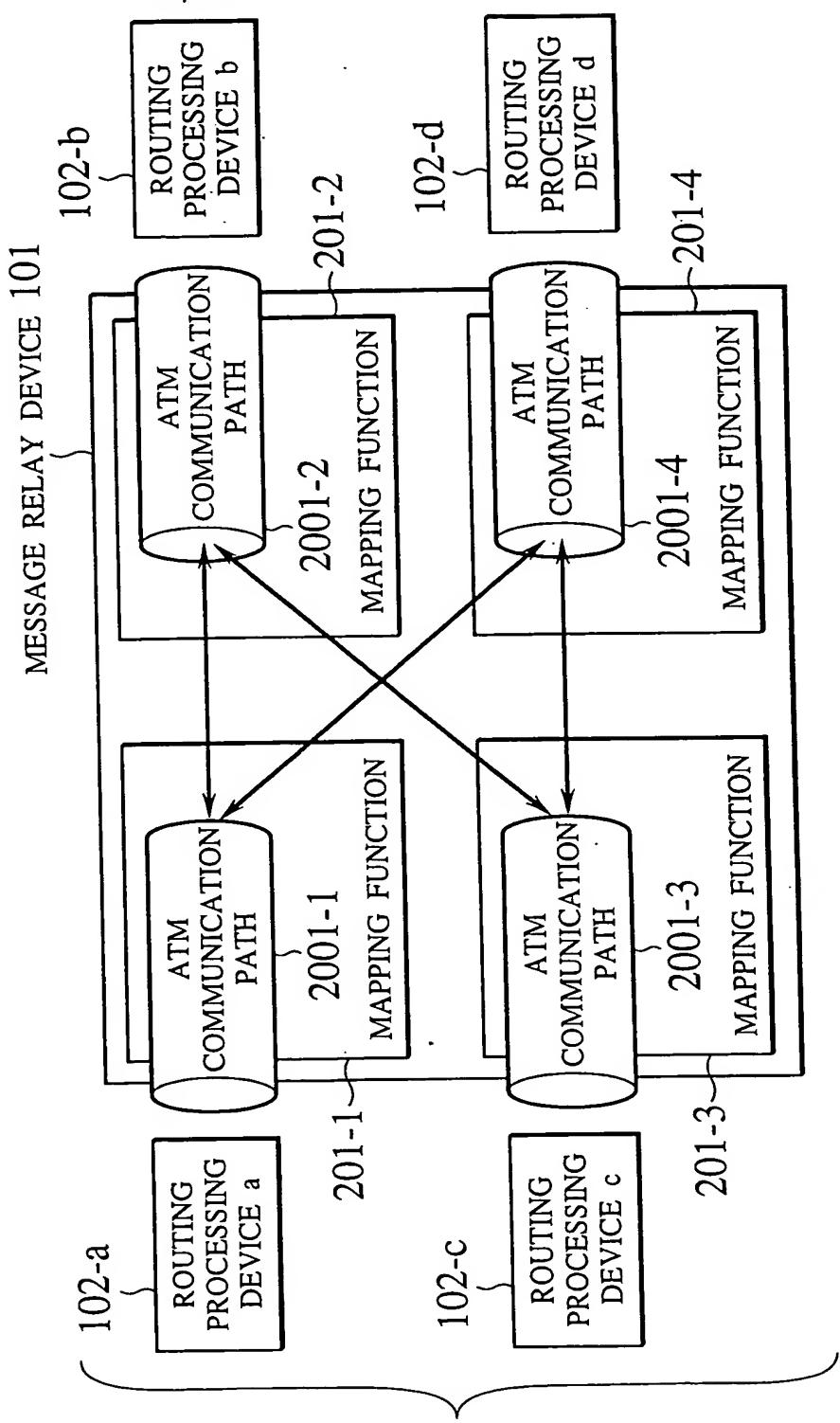
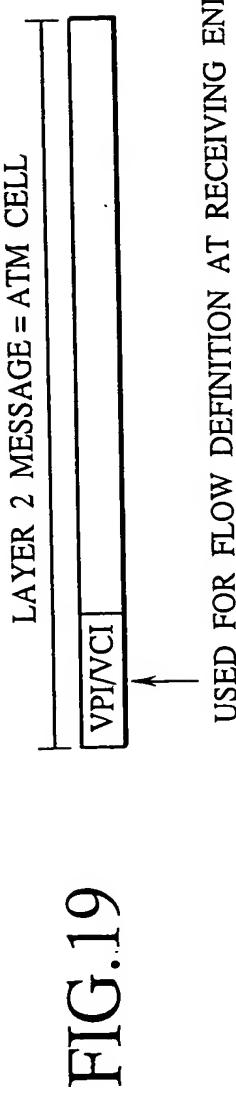


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FIG.21

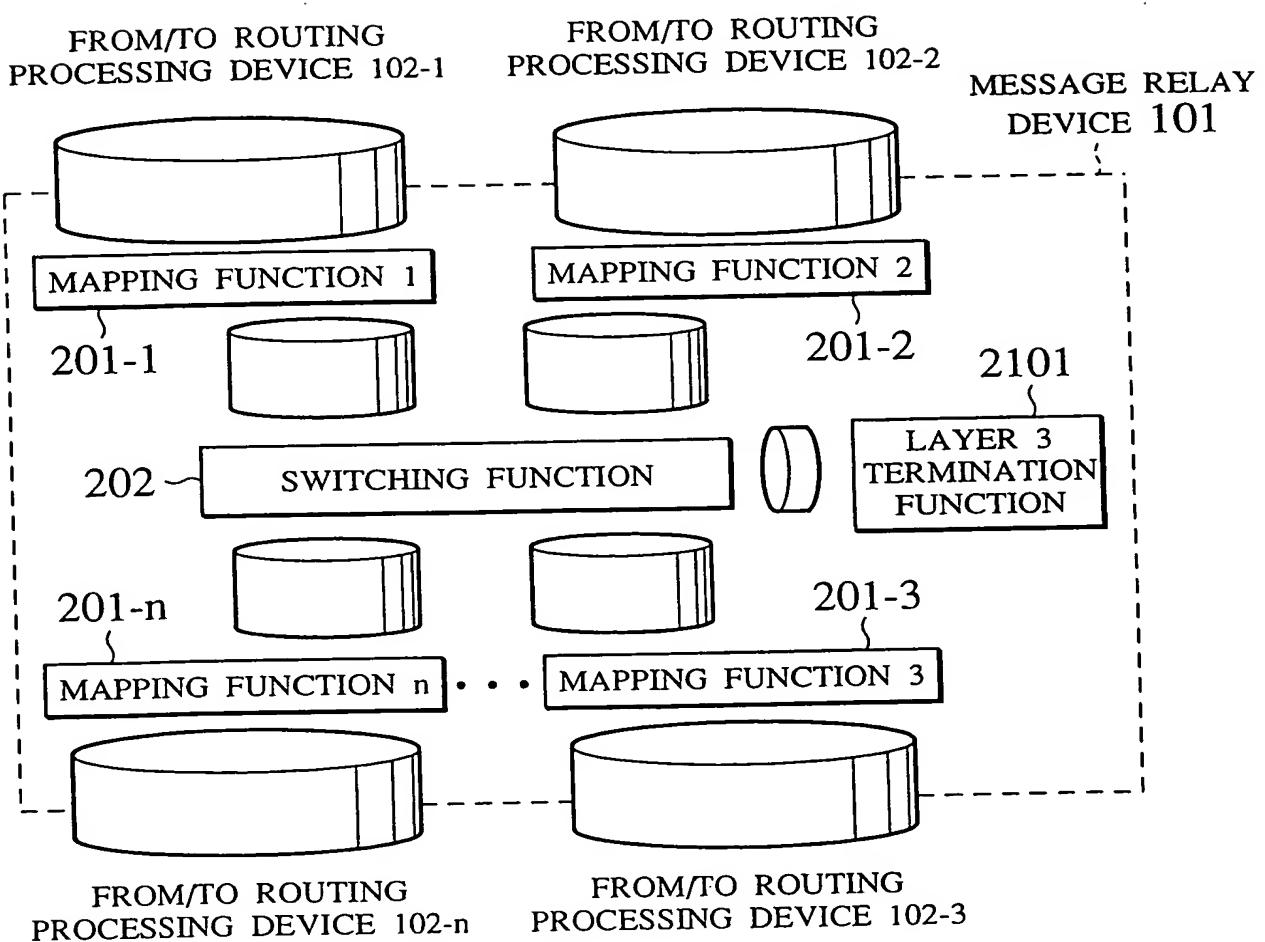


FIG.22

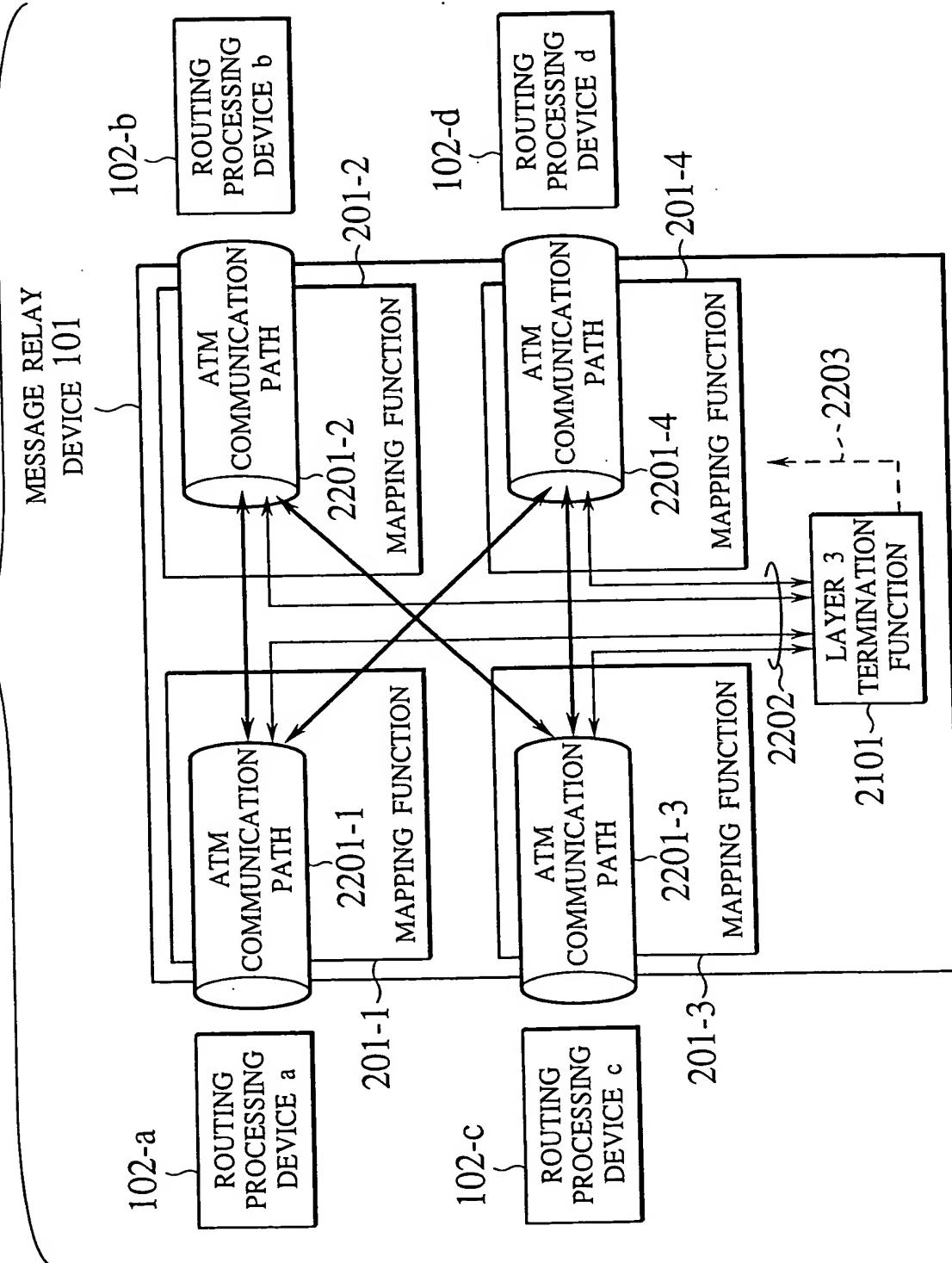


FIG.23

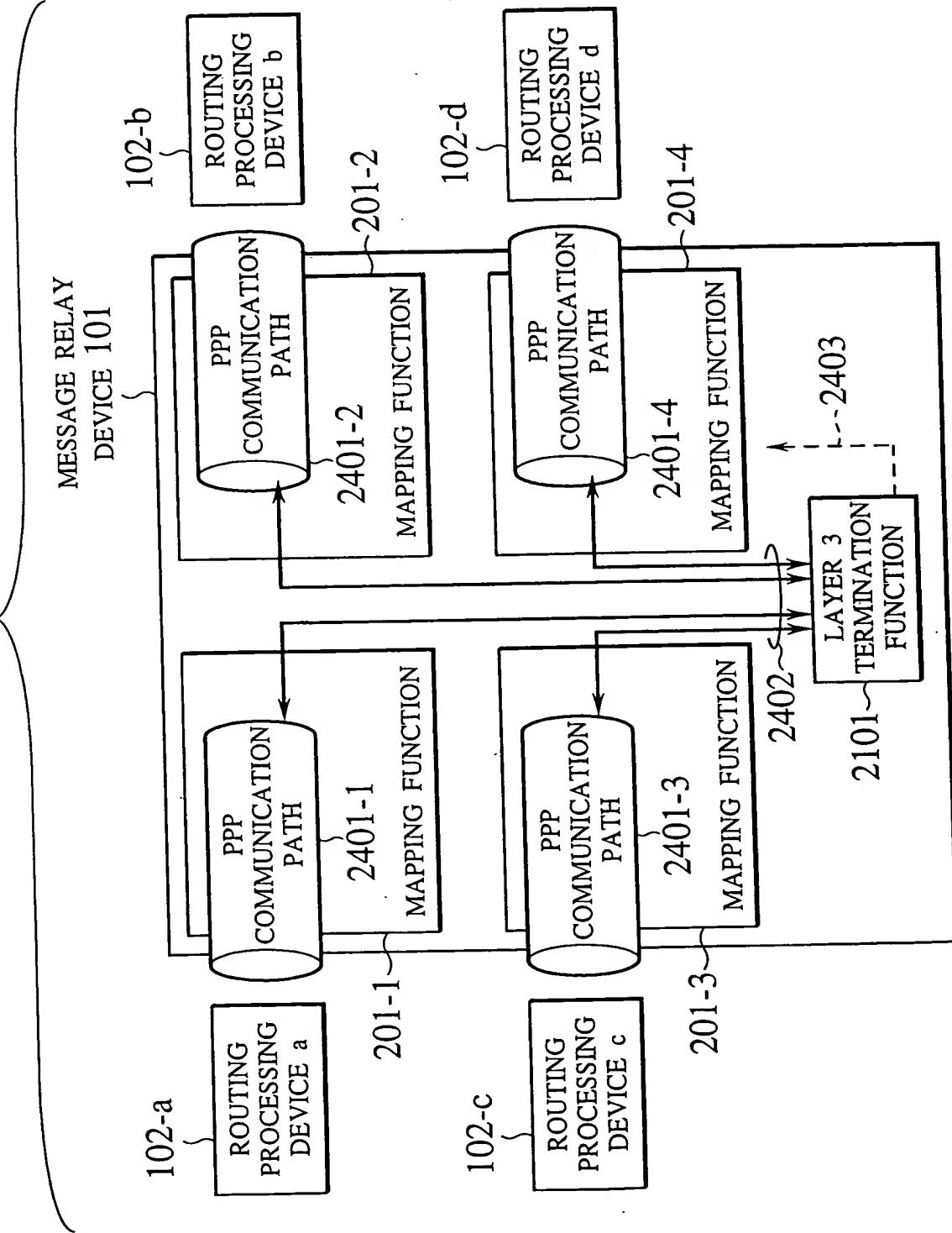


FIG.24

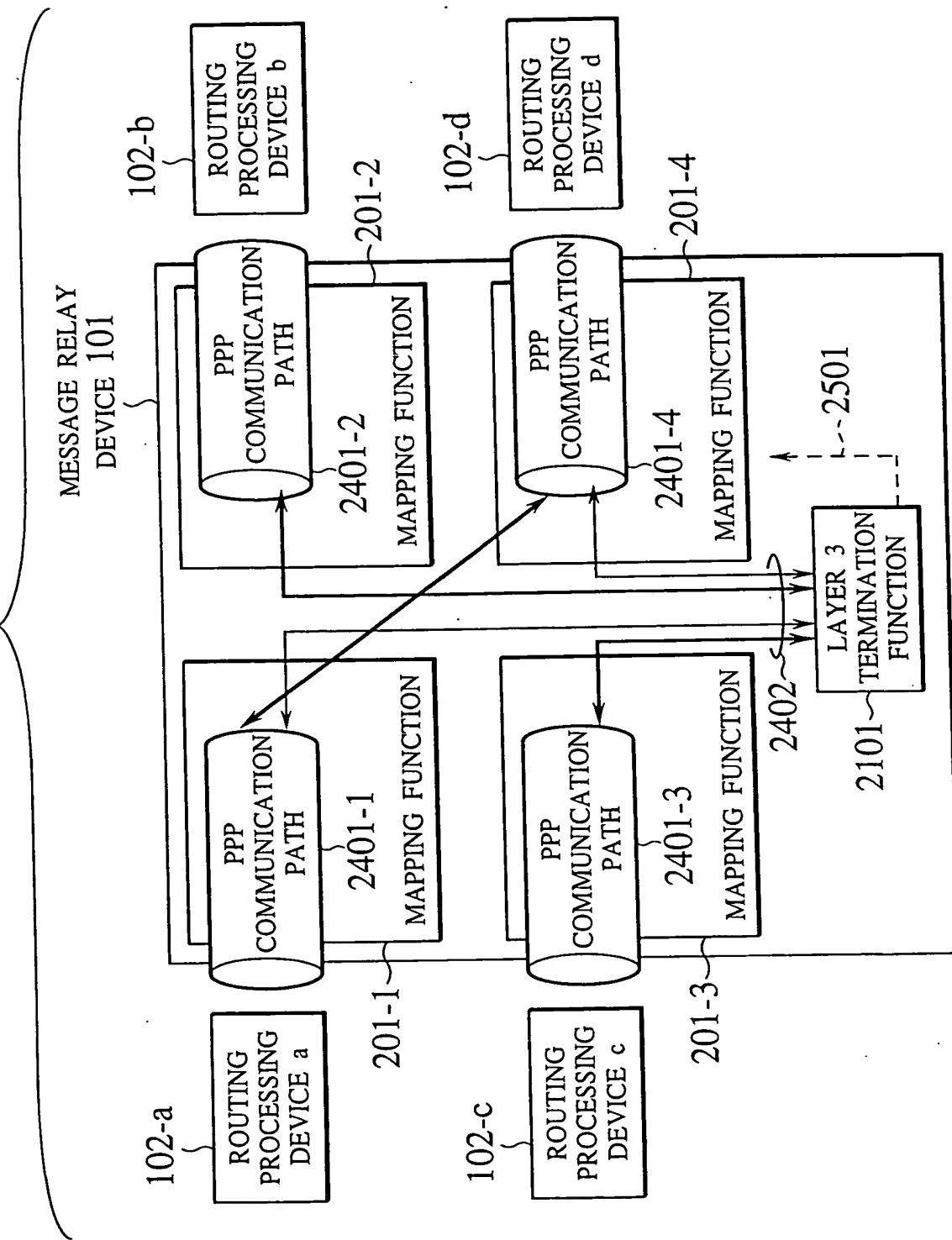
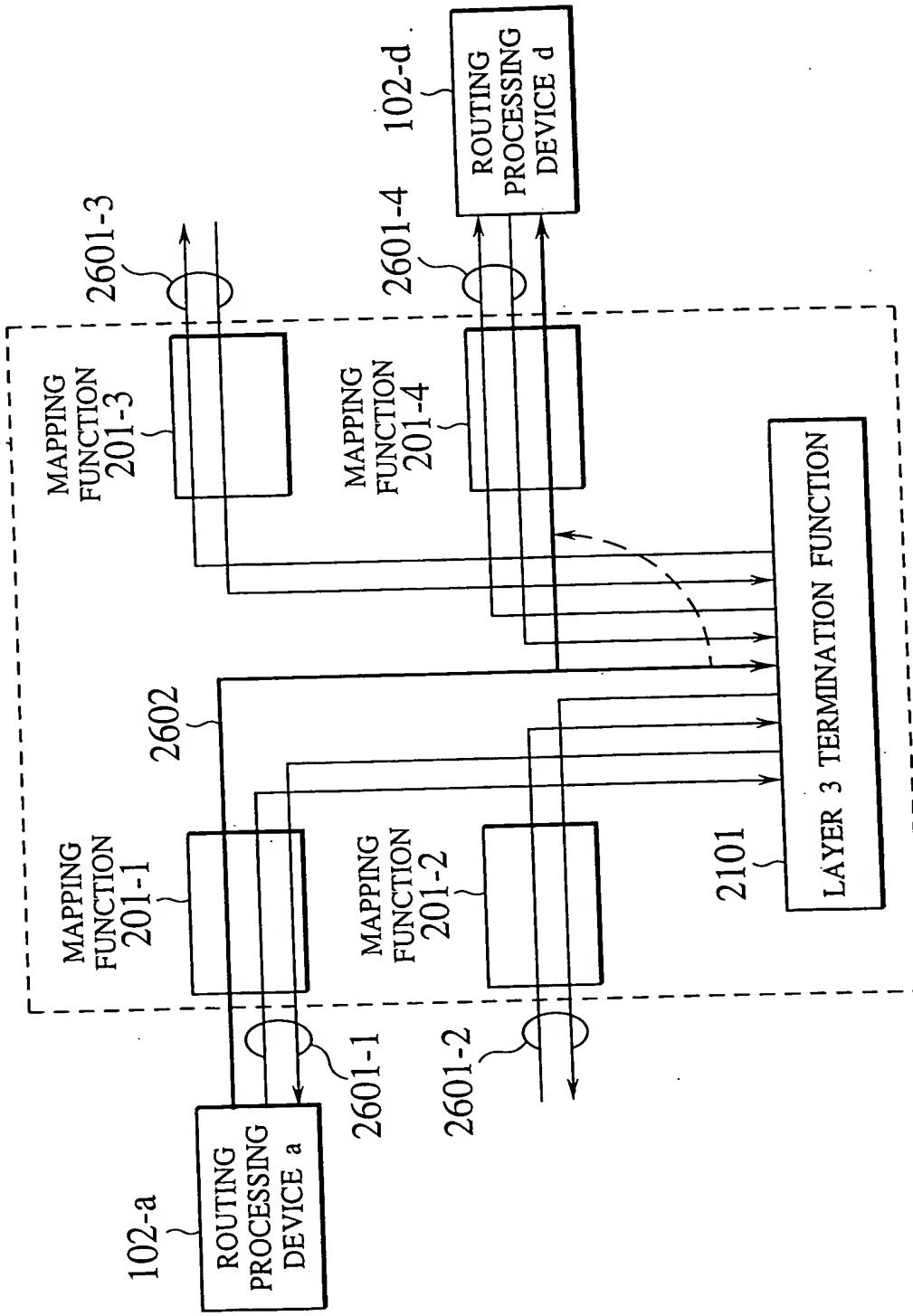


FIG.25

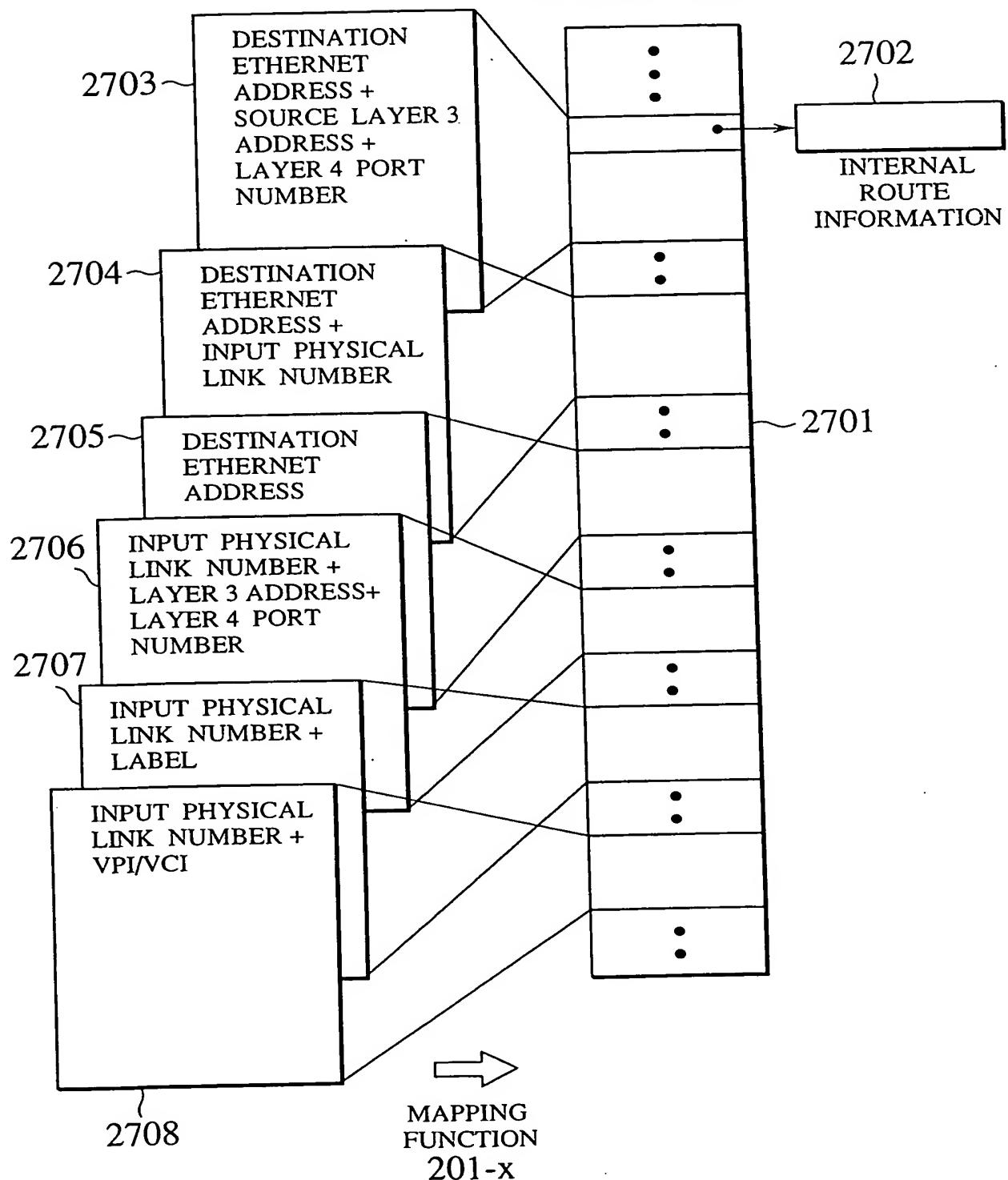
MESSAGE RELAY DEVICE 101



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FIG.26

INTERNAL LABEL



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FIG.27

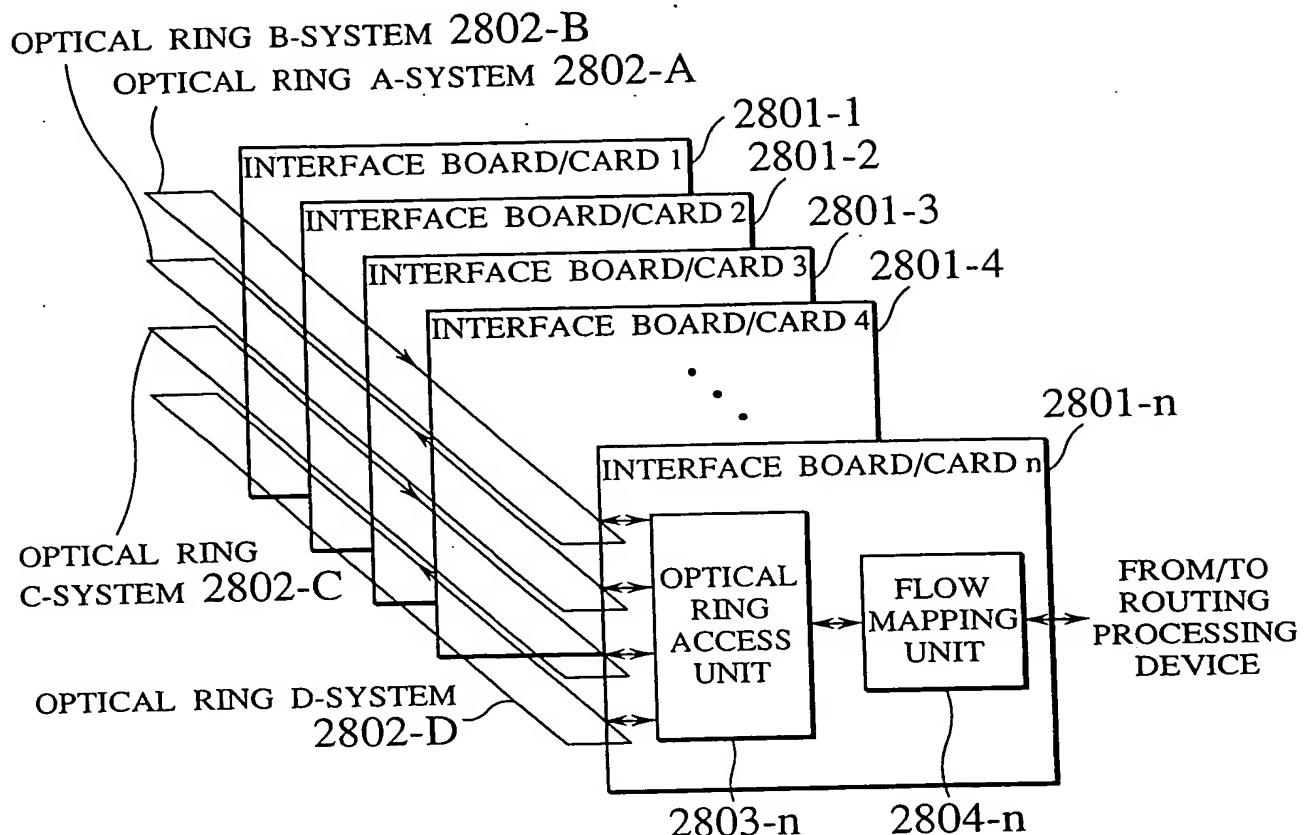
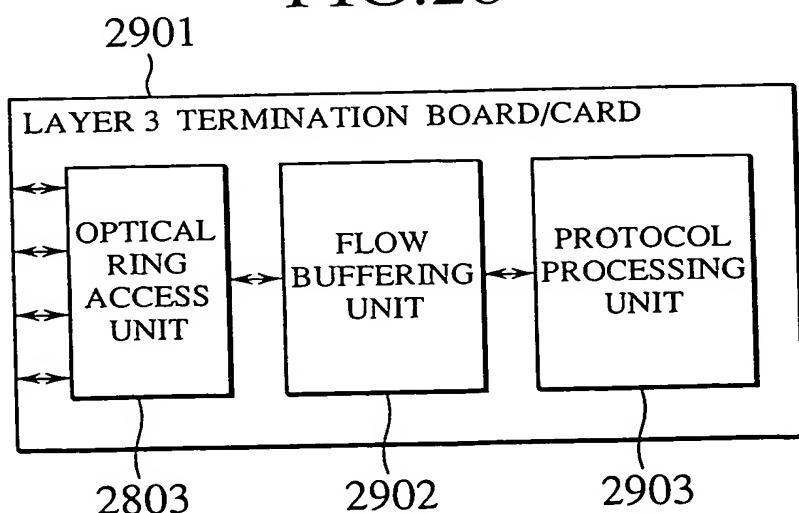


FIG.28



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FIG.29

MESSAGE RELAY DEVICE 101

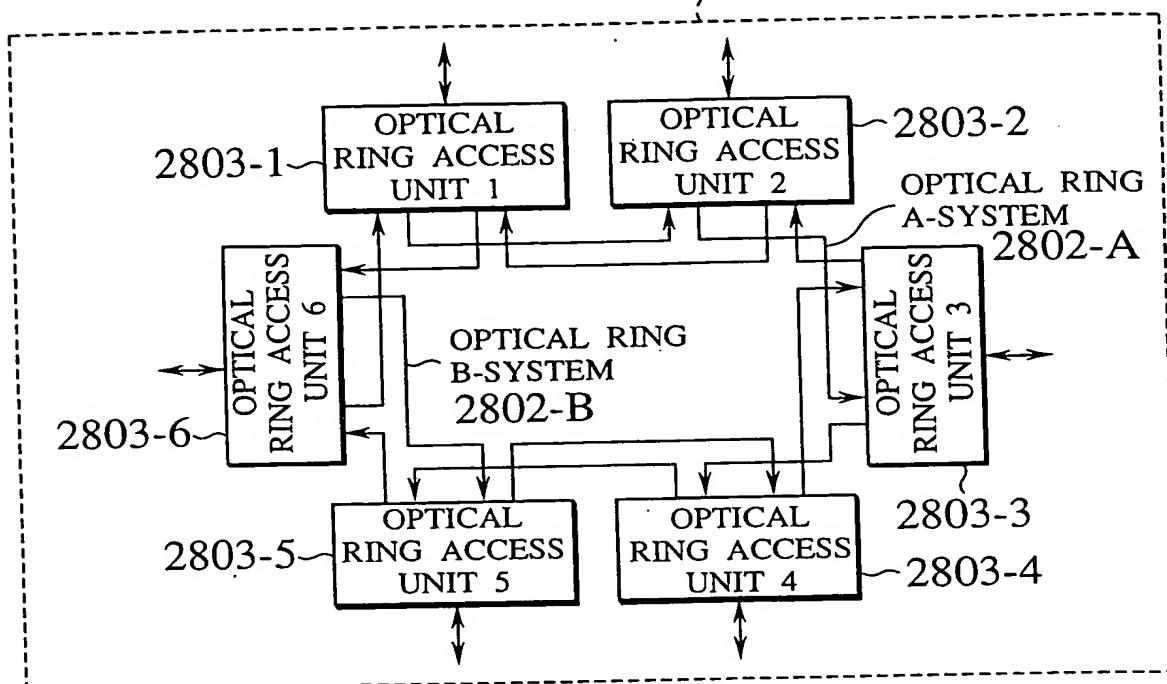
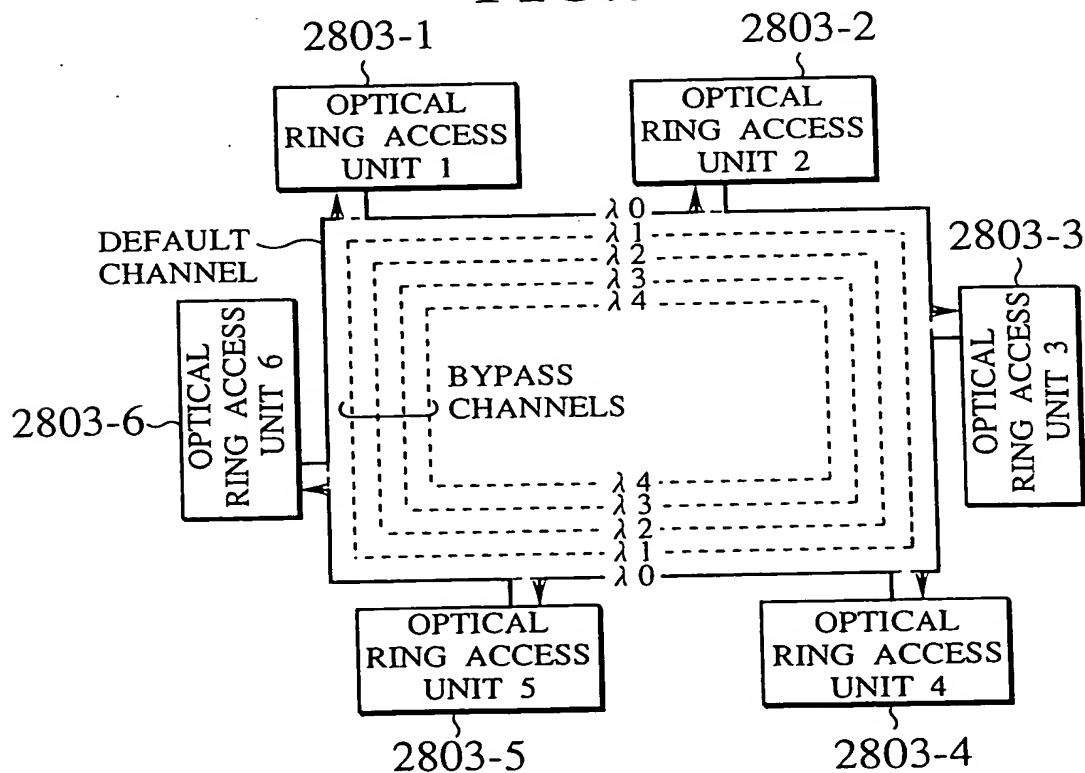


FIG.30



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FIG.31

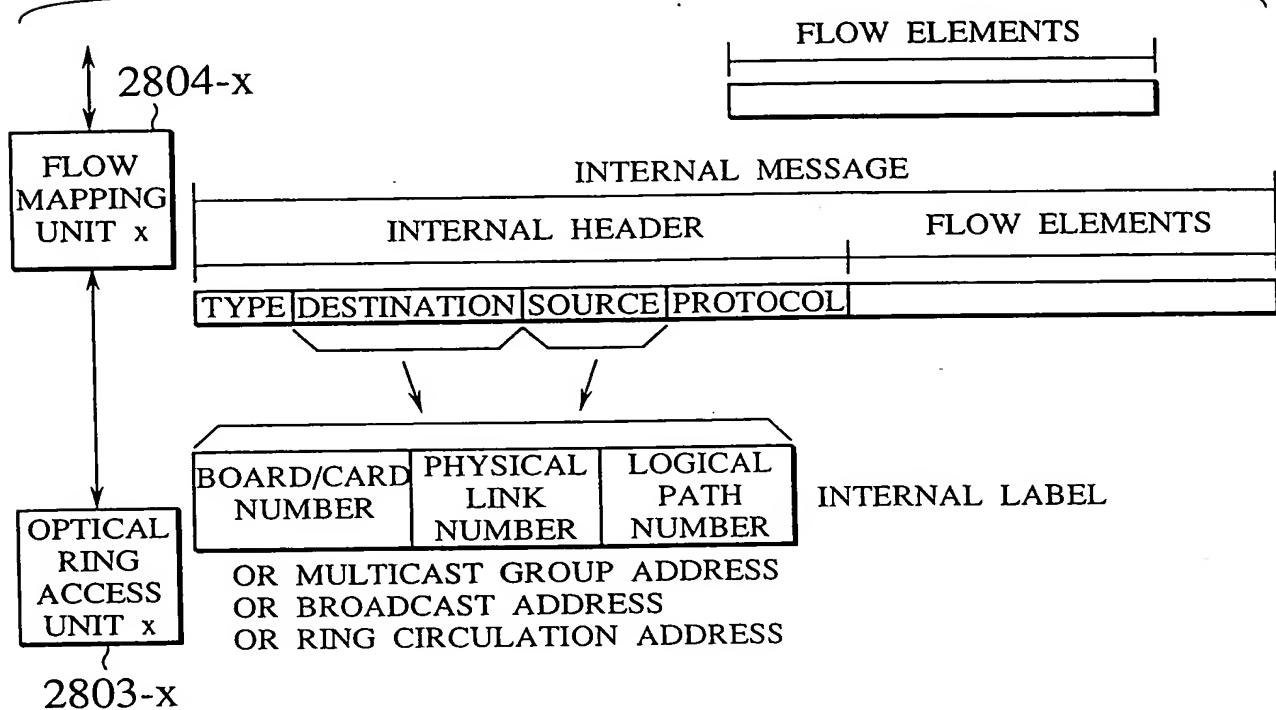


FIG.32

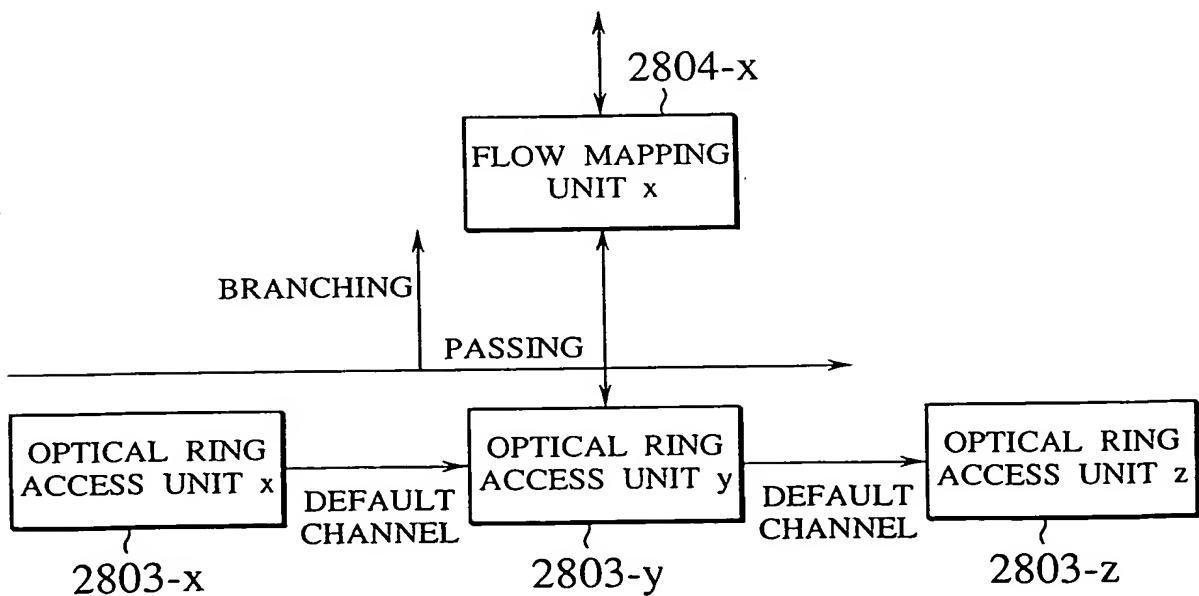
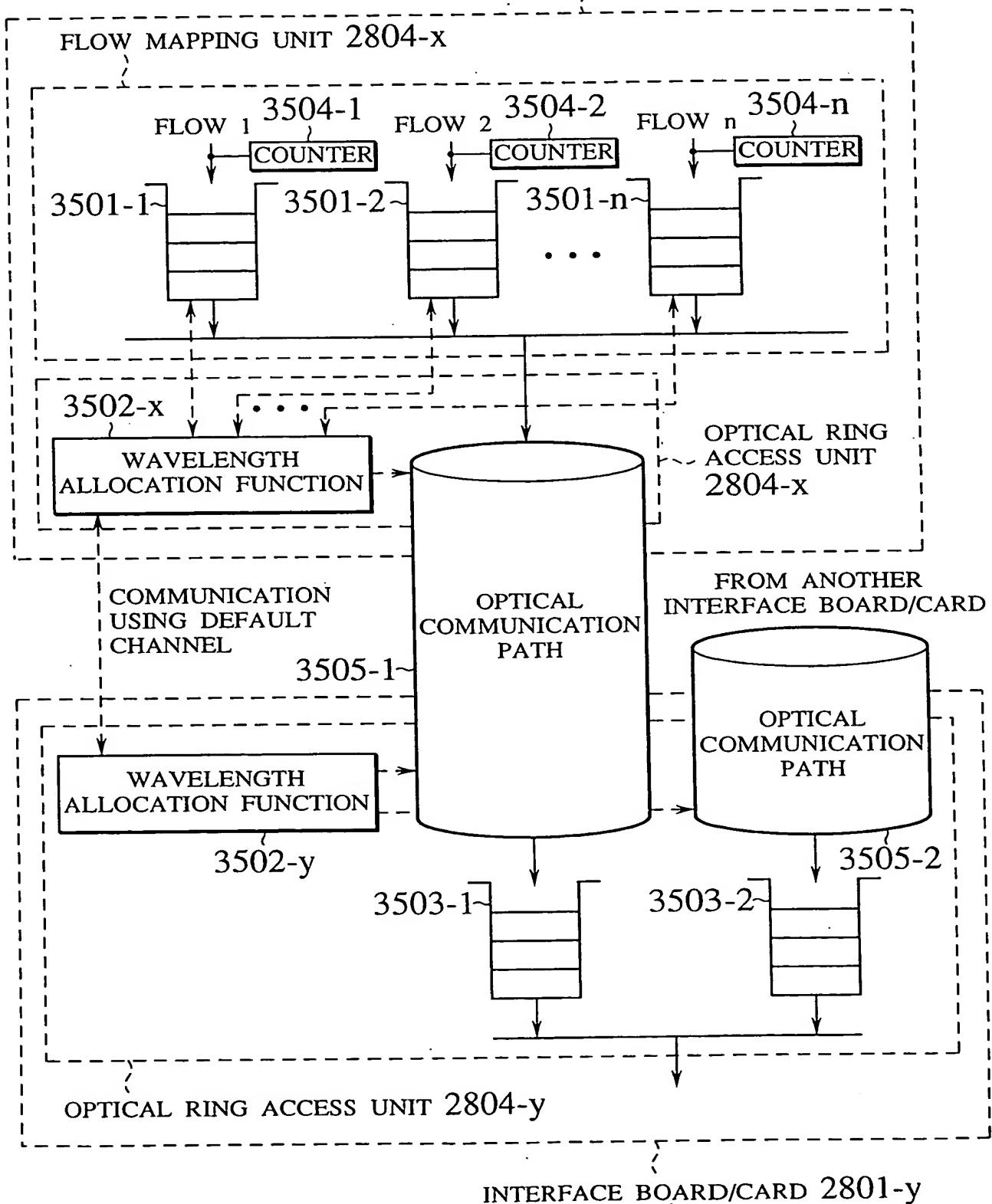


FIG.33

INTERFACE BOARD/CARD 2801-x



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FIG.34A

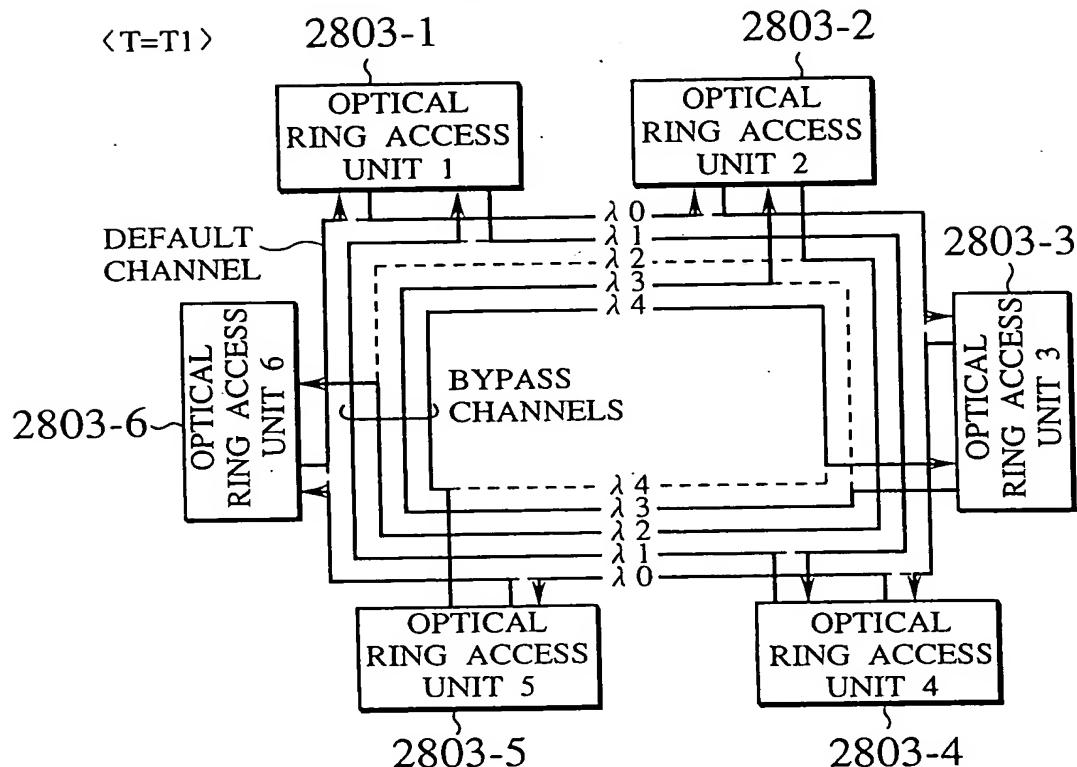
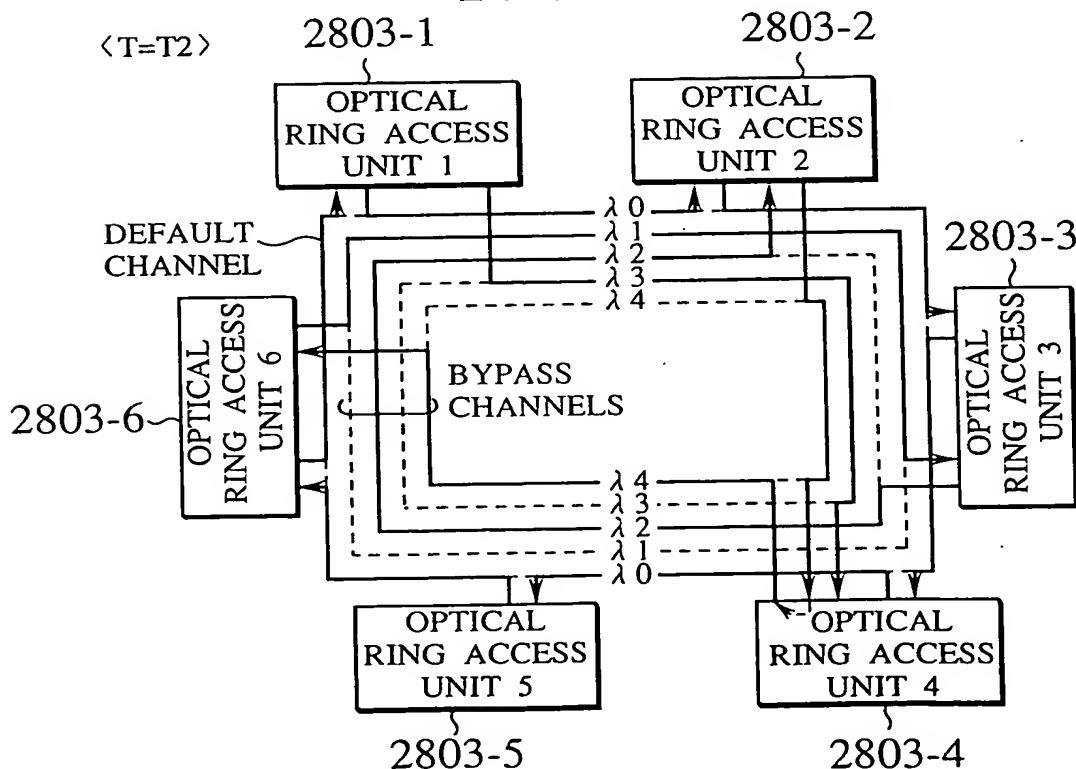


FIG.34B



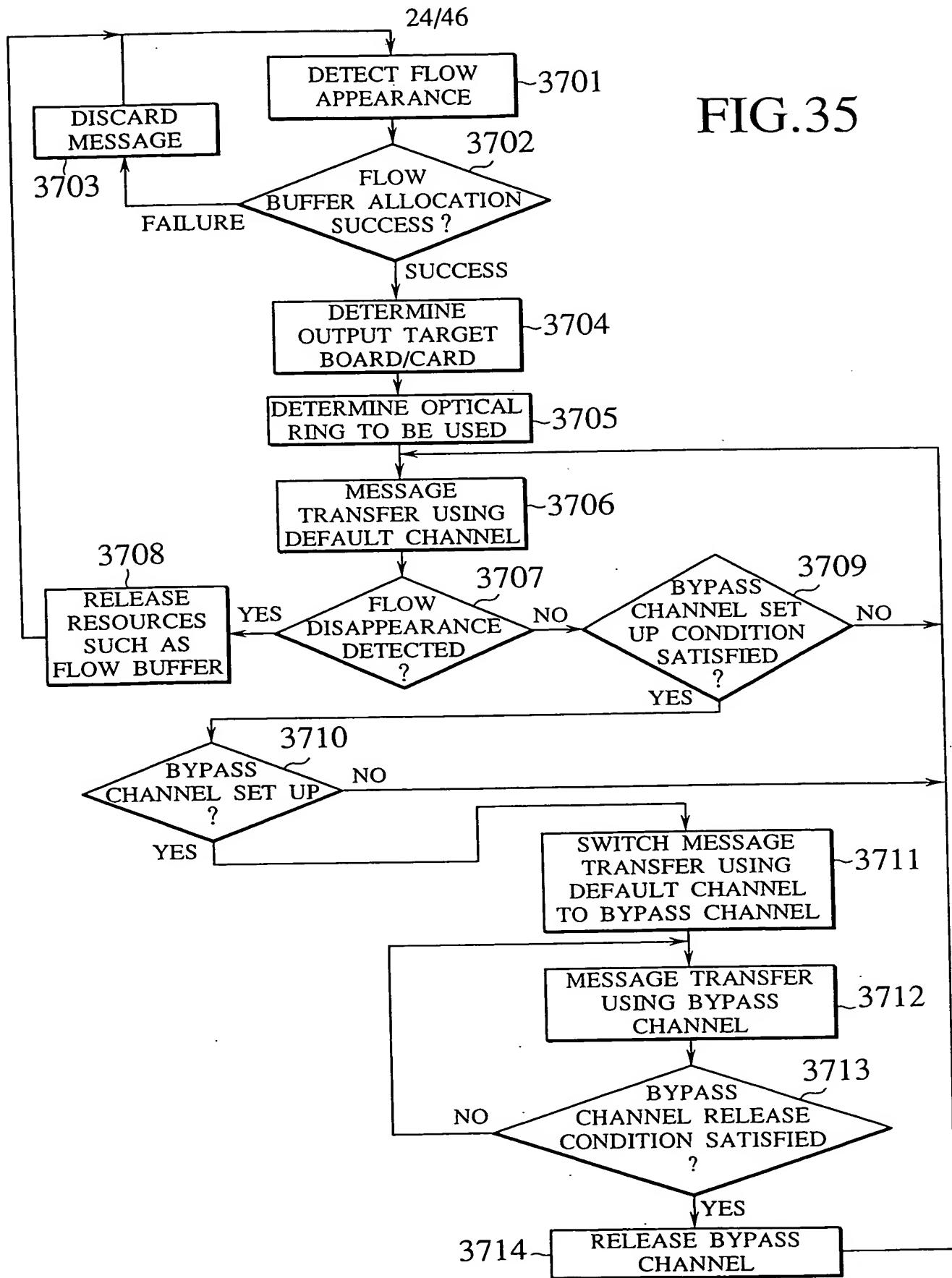
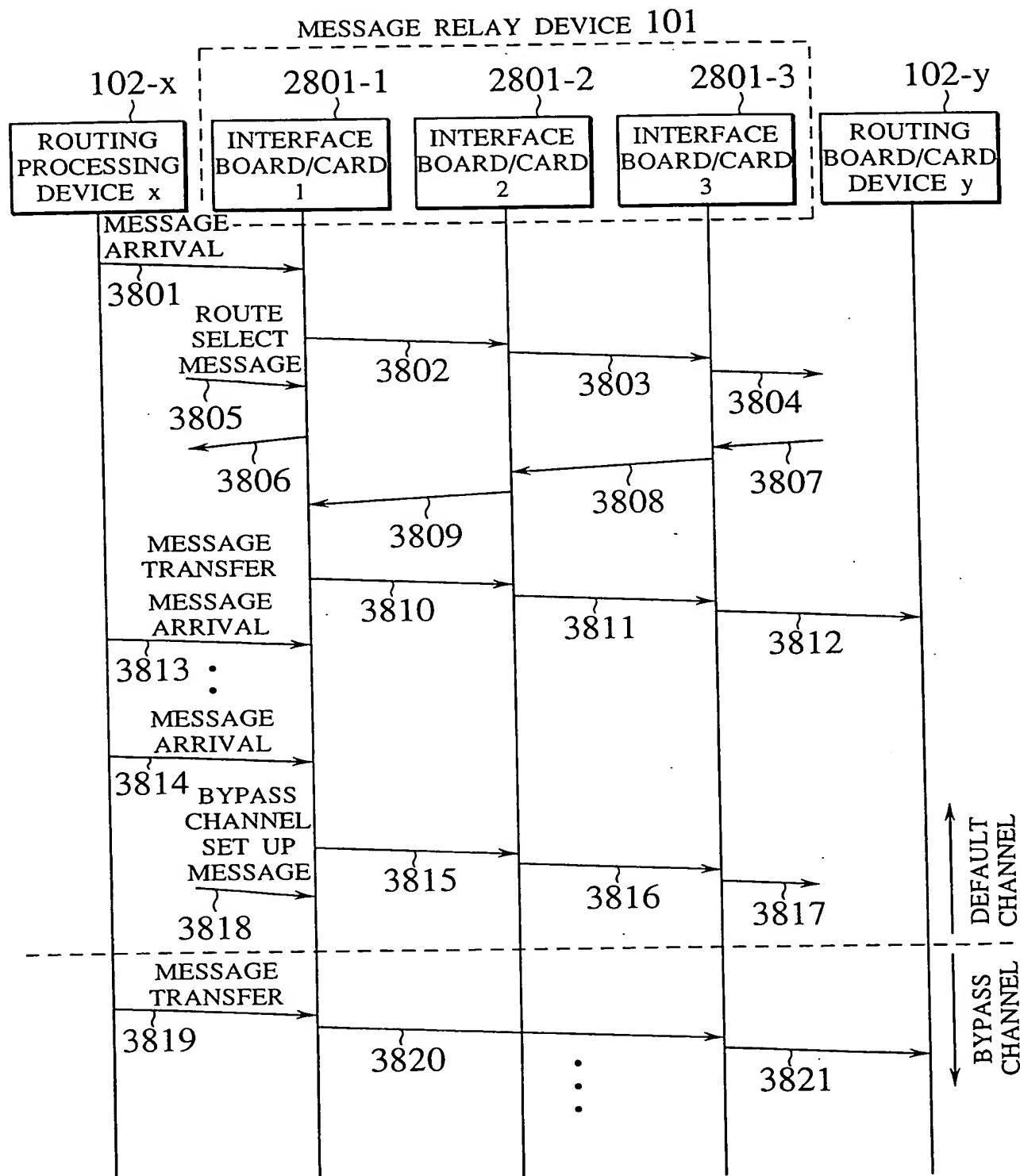


FIG.35

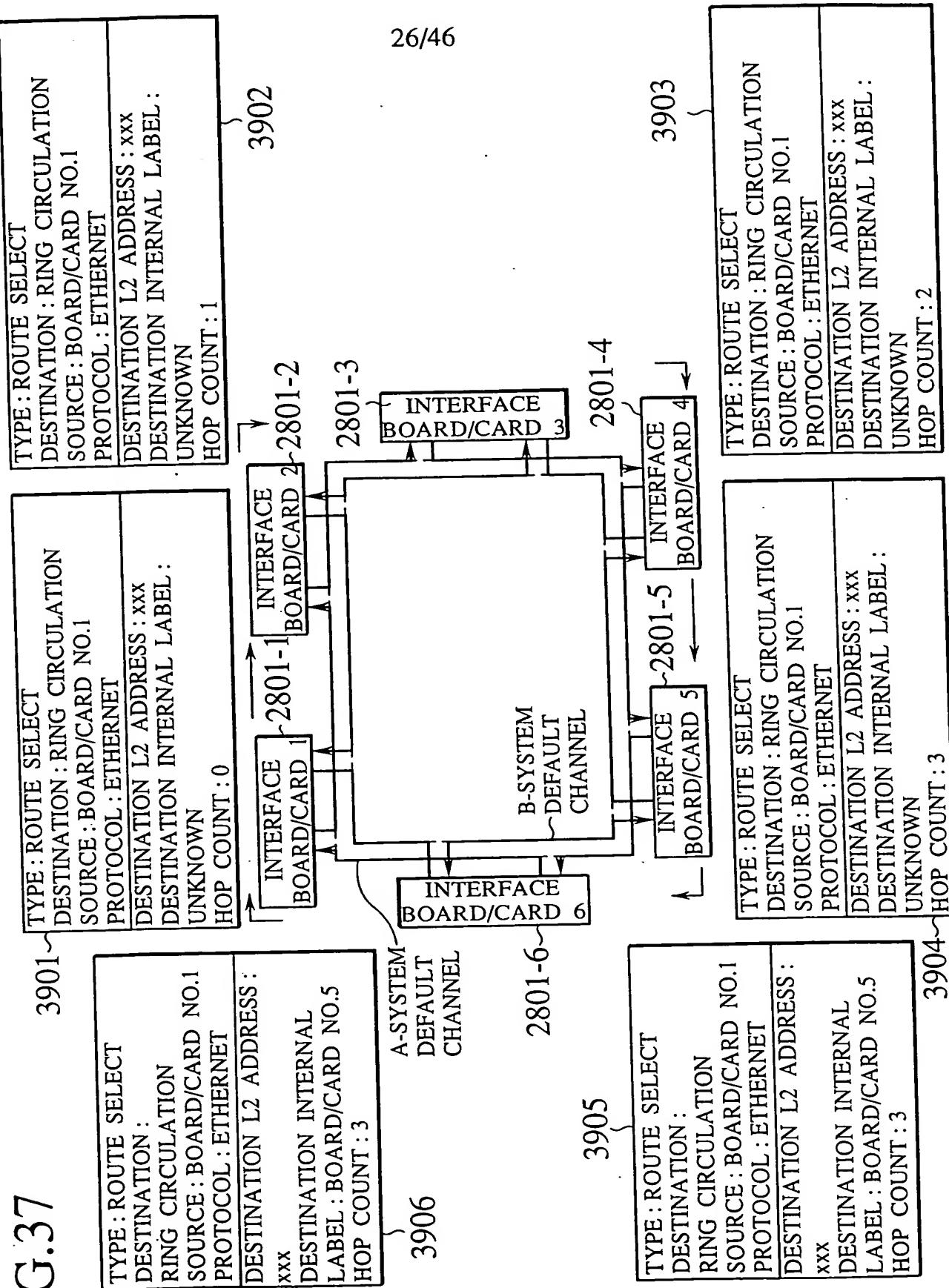
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FIG.36



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FIG.37



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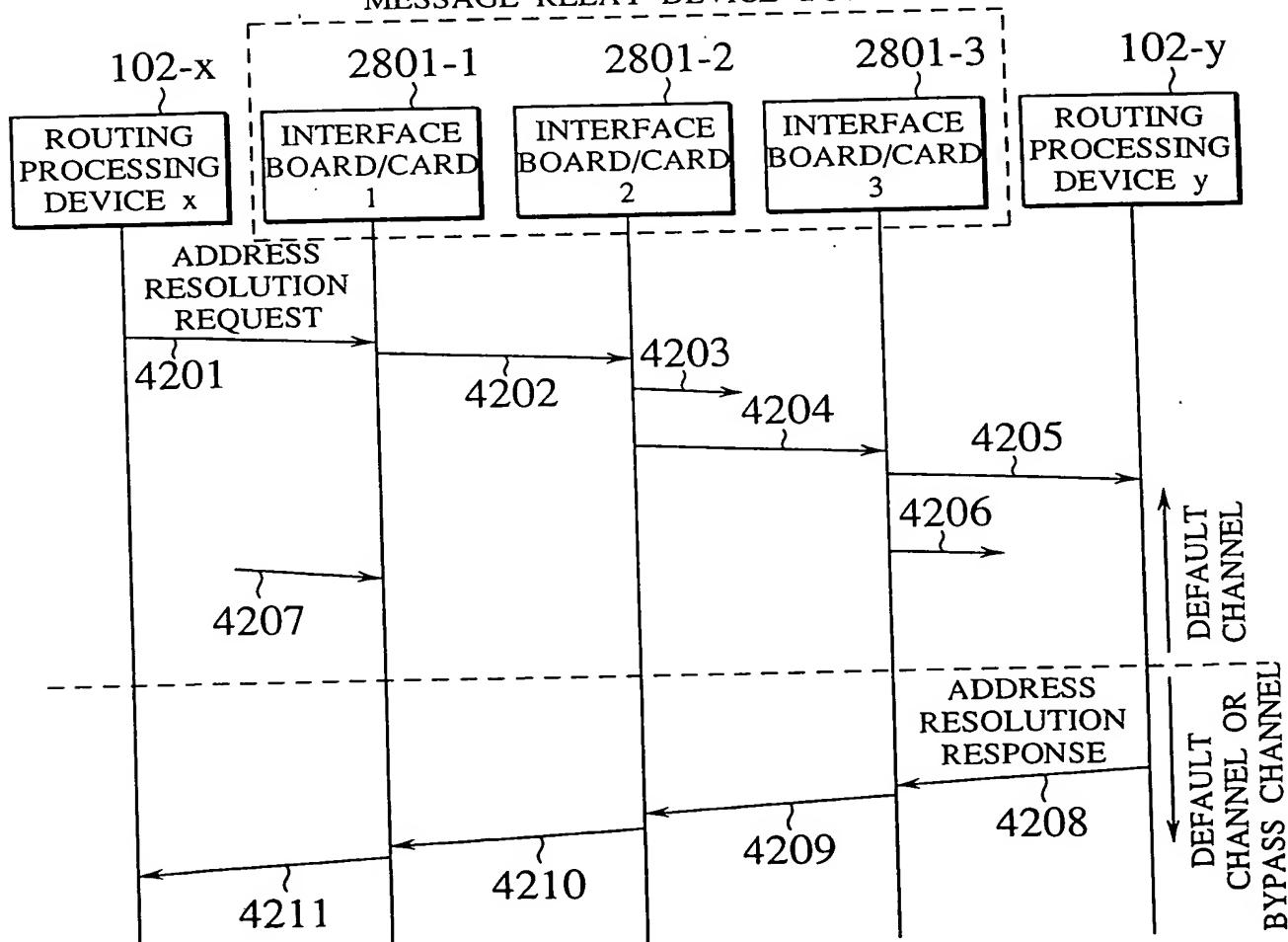
FIG.38

INTERNAL ROUTE INFORMATION HOLDING TABLE 4101

INTERNAL LABEL	CLOCKWISE	A-SYSTEM	BYPASS CHANNEL
	NOT SEARCHED	-----	-----
	PRIMARY ROUTE	SELECTED ROUTE	SELECTED CHANNEL

FIG.39

MESSAGE RELAY DEVICE 101



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FIG.40

WAVELENGTH ALLOCATION TABLE

λ_0	DEFAULT CHANNEL	
λ_1	BOARD/CARD 1	BOARD/CARD 4
λ_2	BOARD/CARD 3	BOARD/CARD 2
• • •		
λ_1	UNUSED	-----
• • •		
λ_n	BOARD/CARD 2	BOARD/CARD 3

WAVELENGTH TRANSMITTING END RECEIVING END

FIG.41

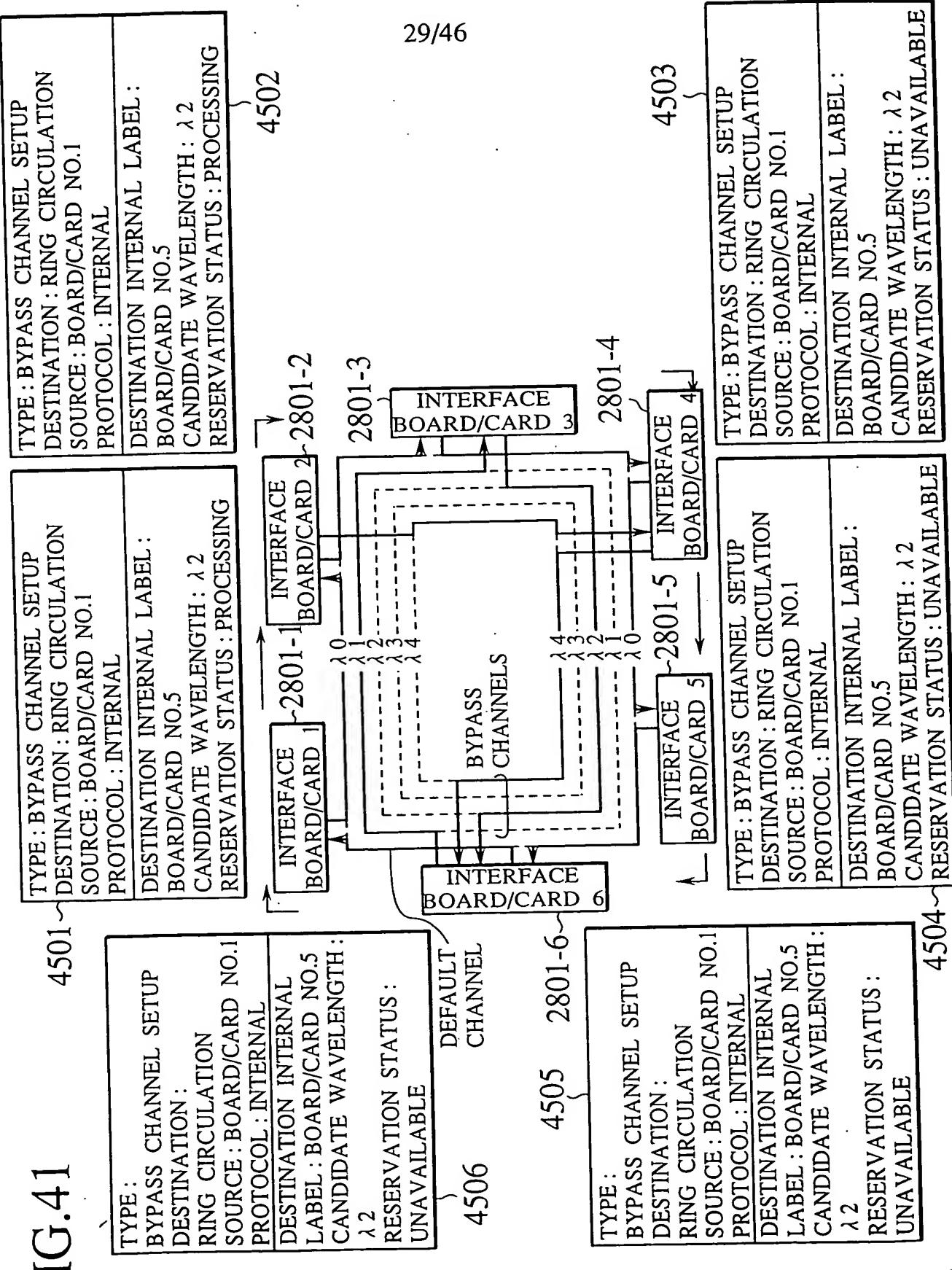
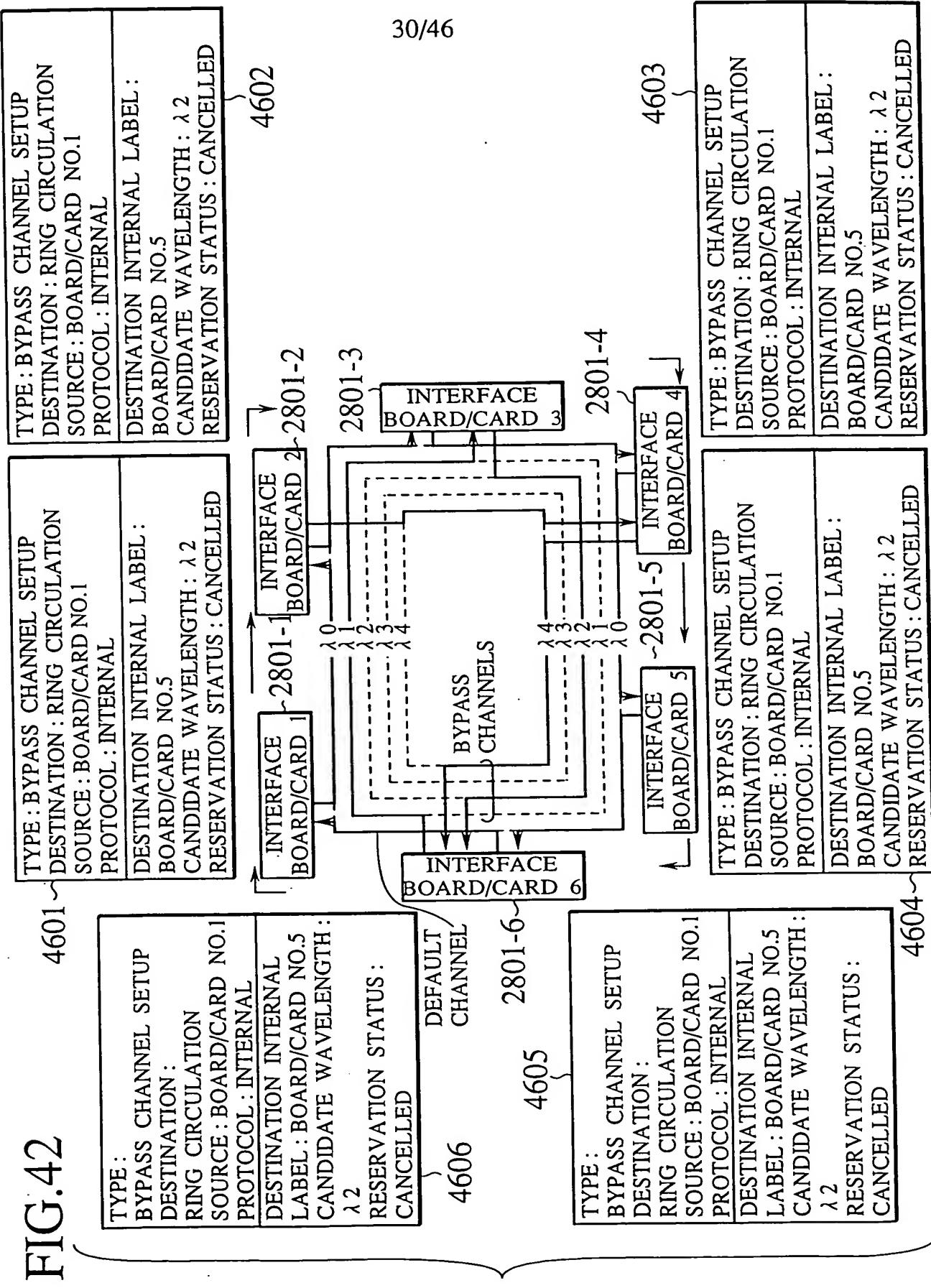


FIG.42



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Title: MESSAGE RELAYING SCHEME
BASED ON SWITCHING IN UNITS OF
FLOWS
Inventor(s): Yasuro SHOBATAKE
DOCKET NO.: 040301-0641

FIG.43

Title: MESSAGE RELAYING SCHEME BASED ON SWITCHING IN UNITS OF FLOWS

Inventor(s): Yasuro SHOBATAKE
DOCKET NO.: 040301-0641

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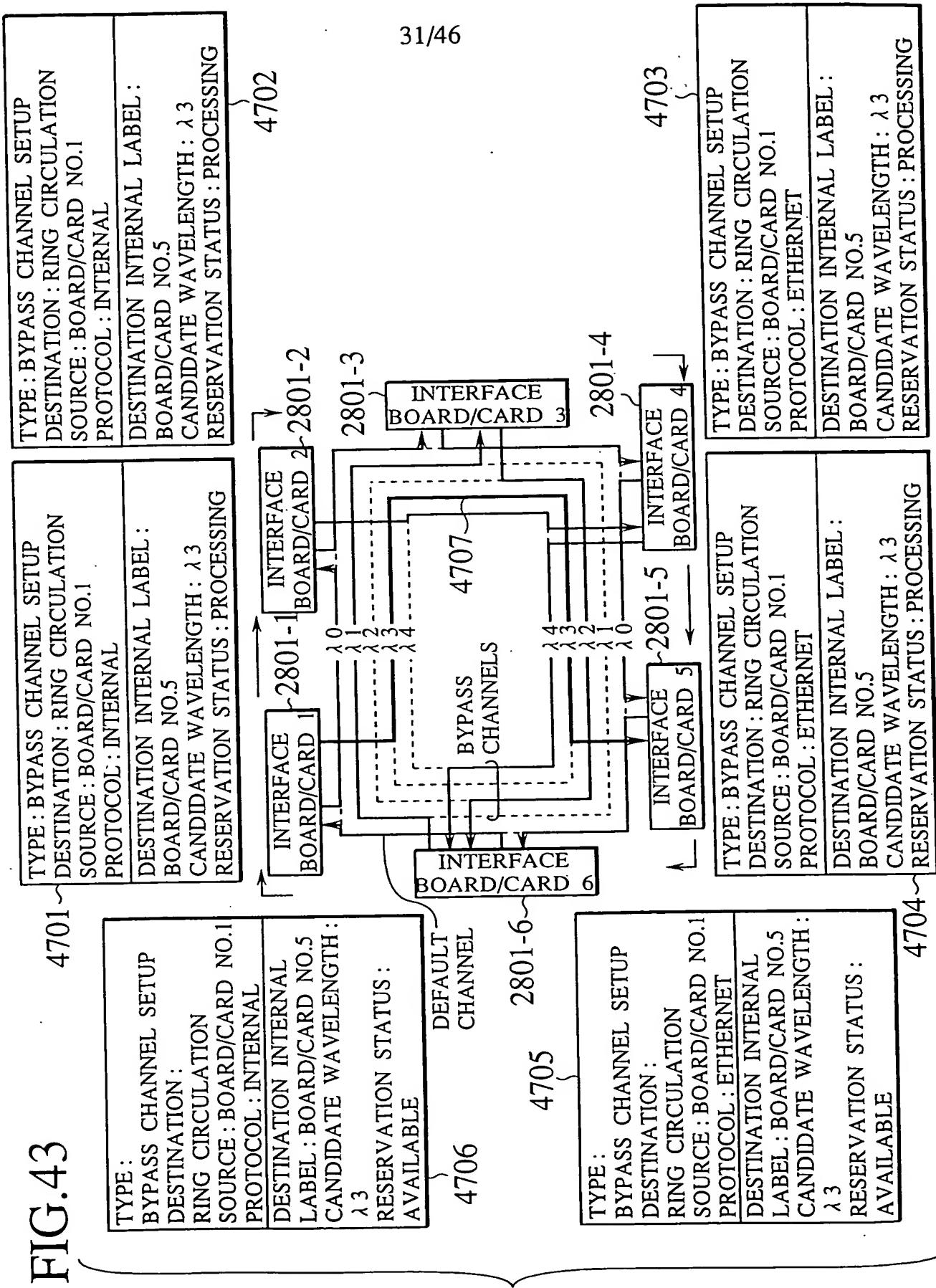


FIG.44

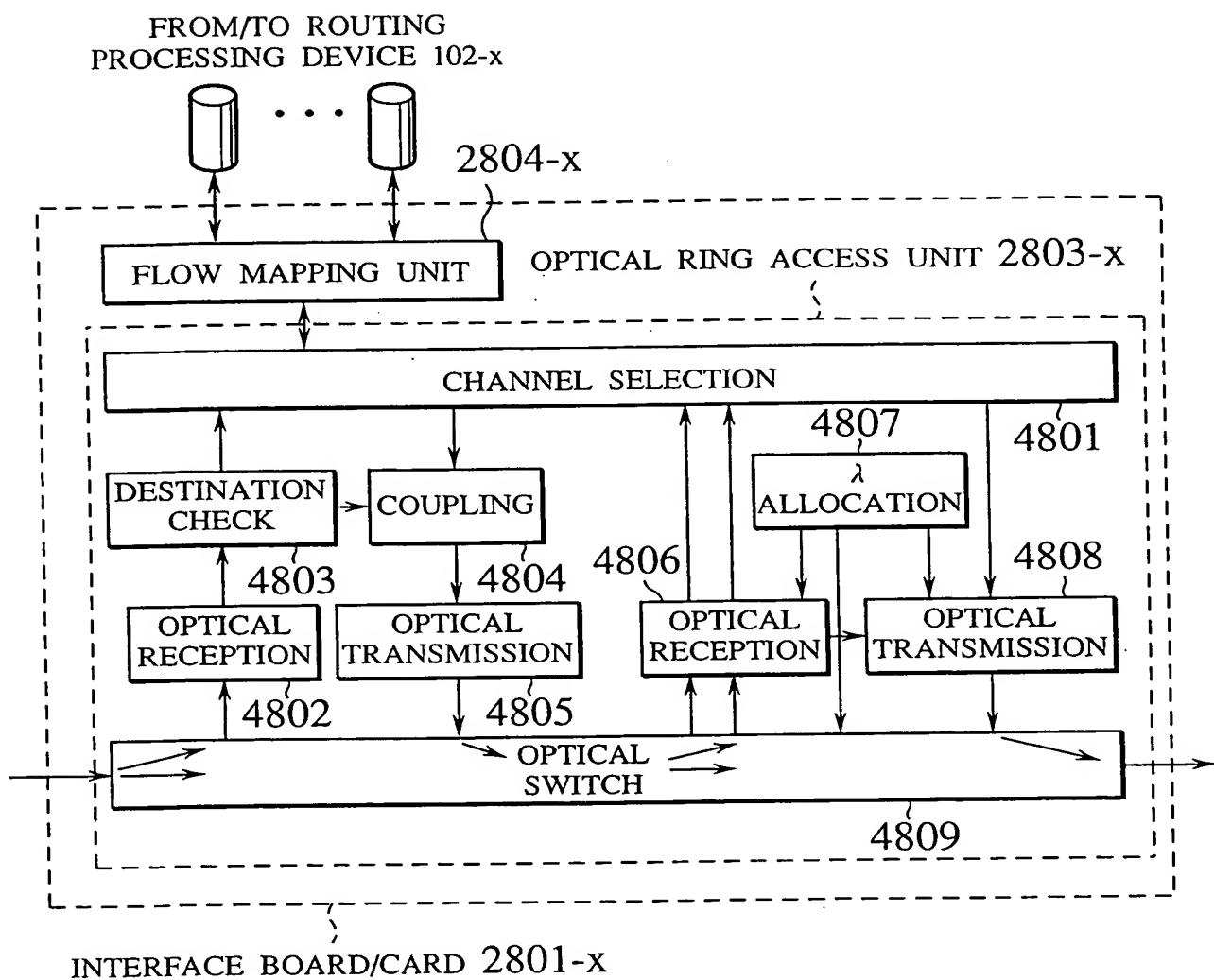


FIG.45

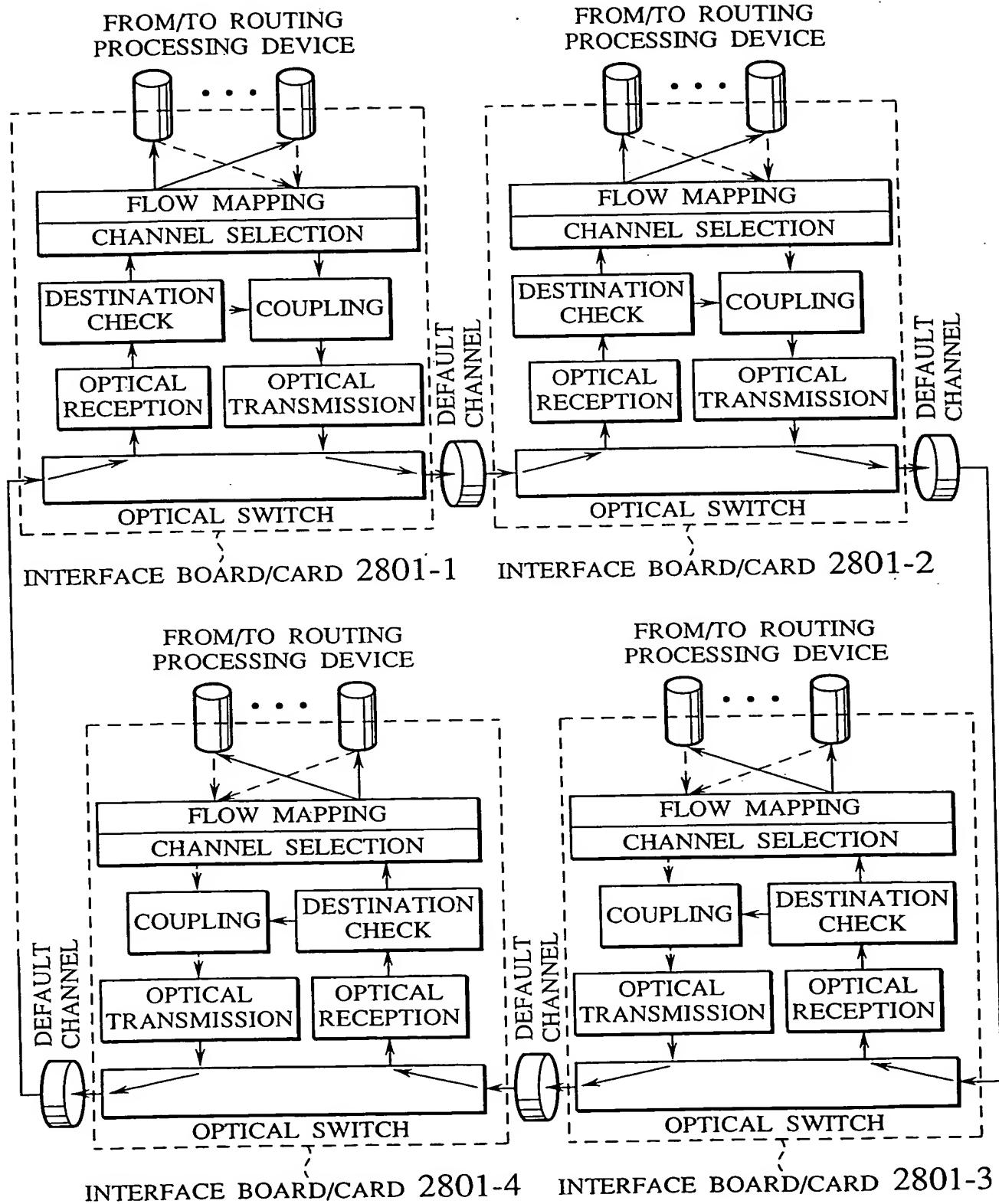


FIG.46

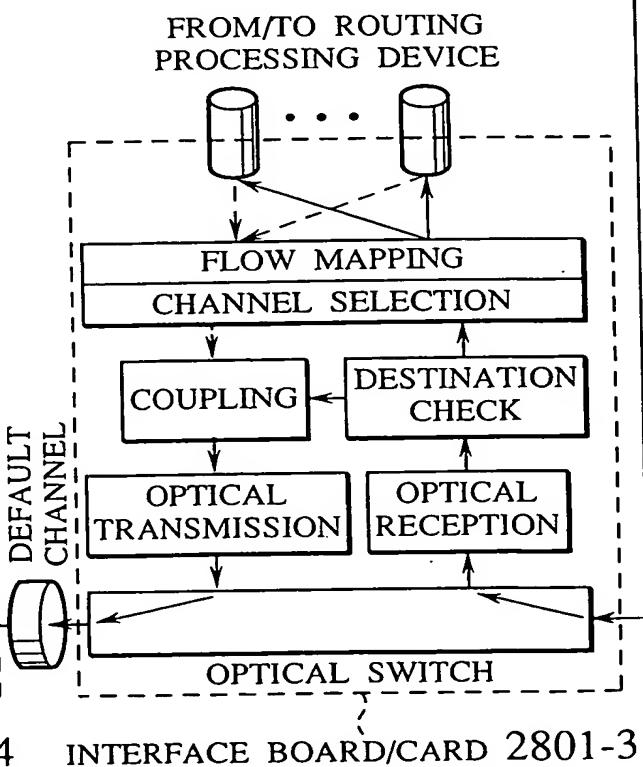
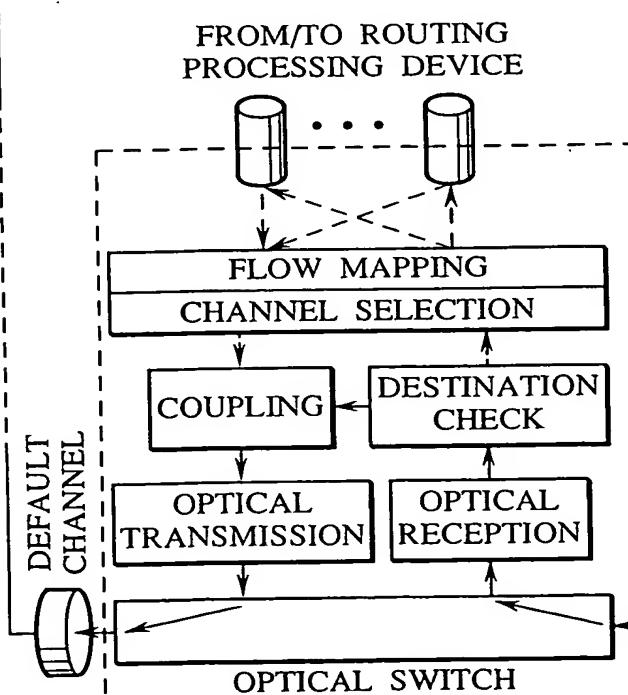
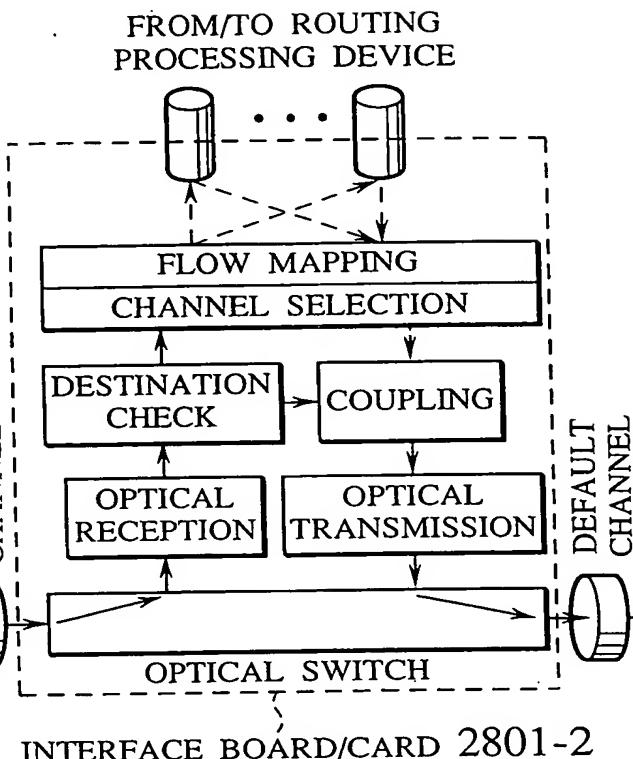
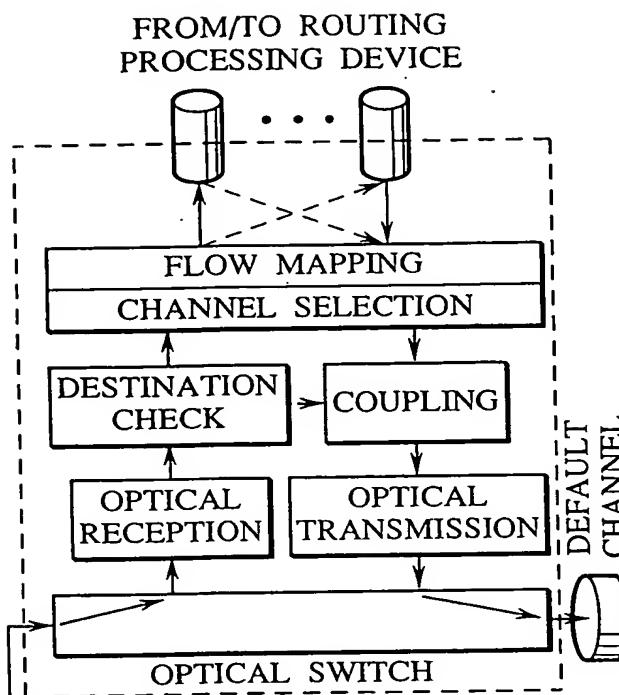


FIG. 47

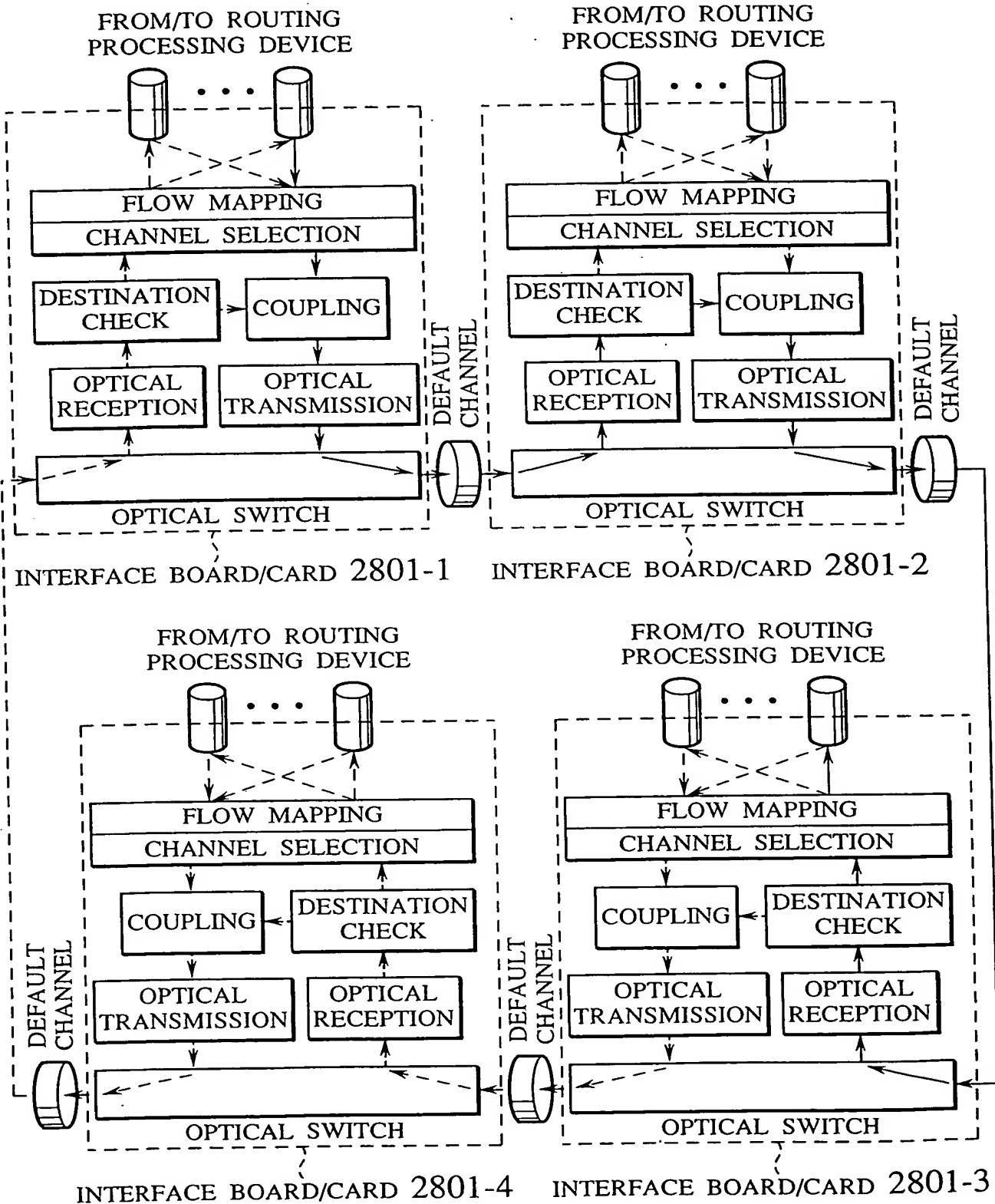
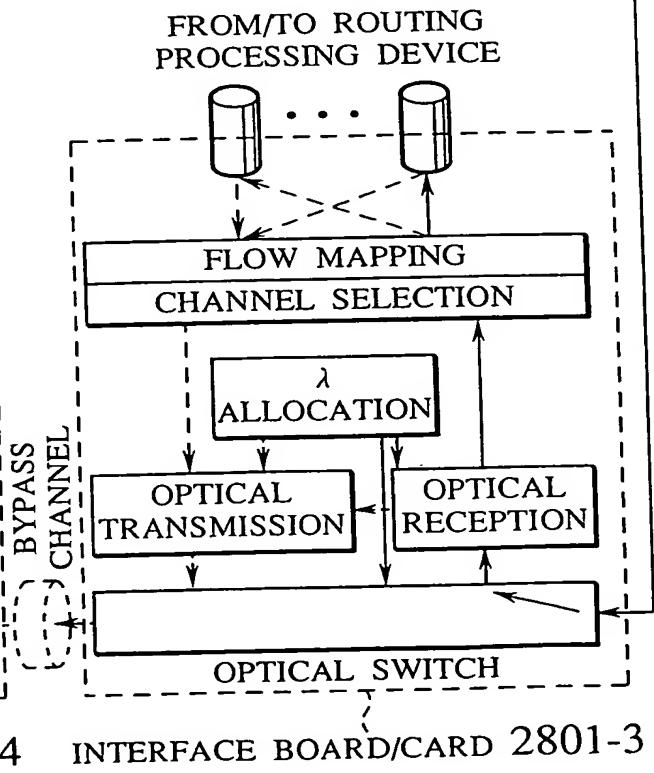
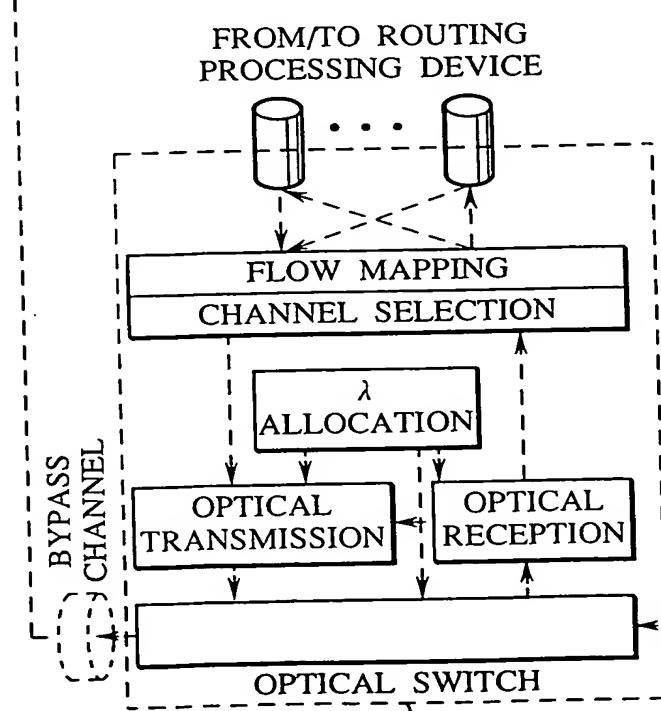
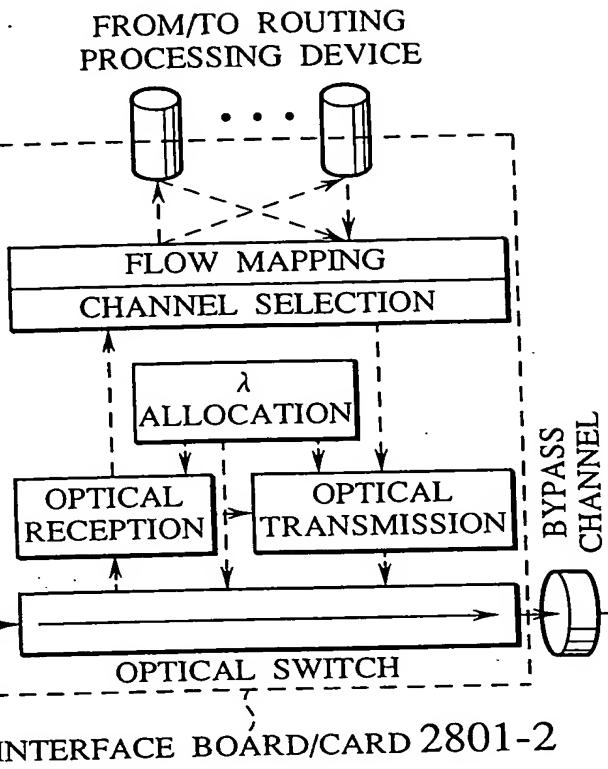
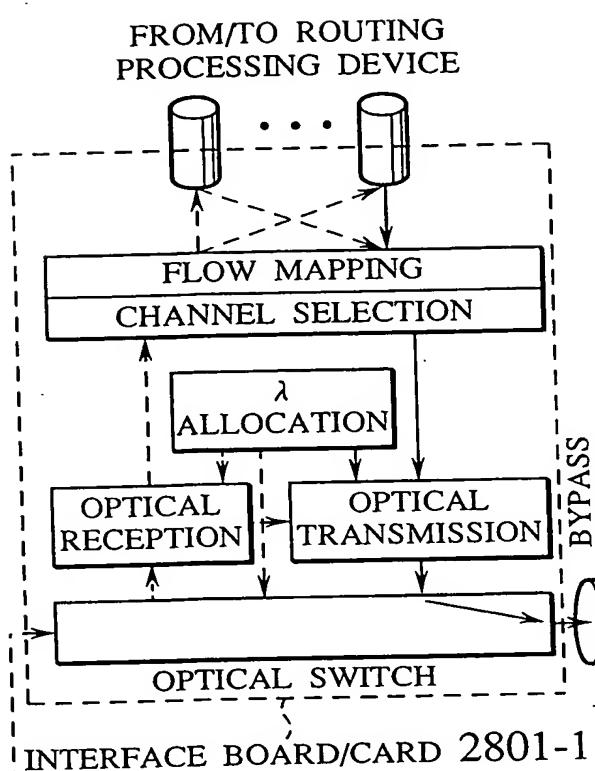
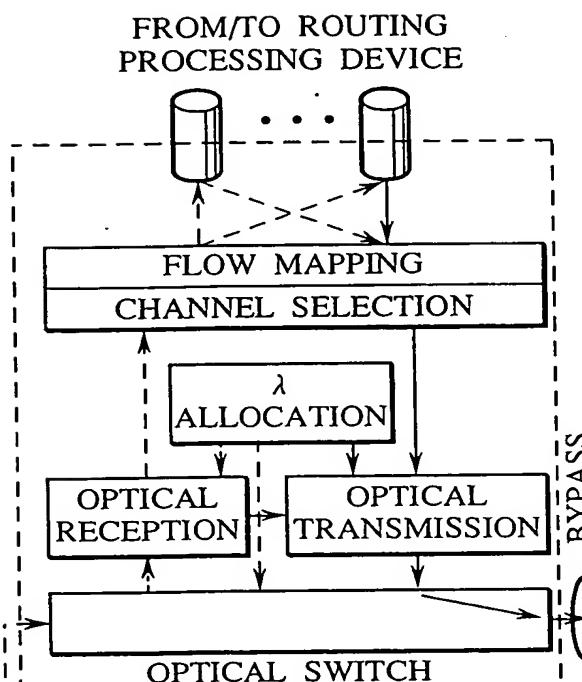


FIG.48

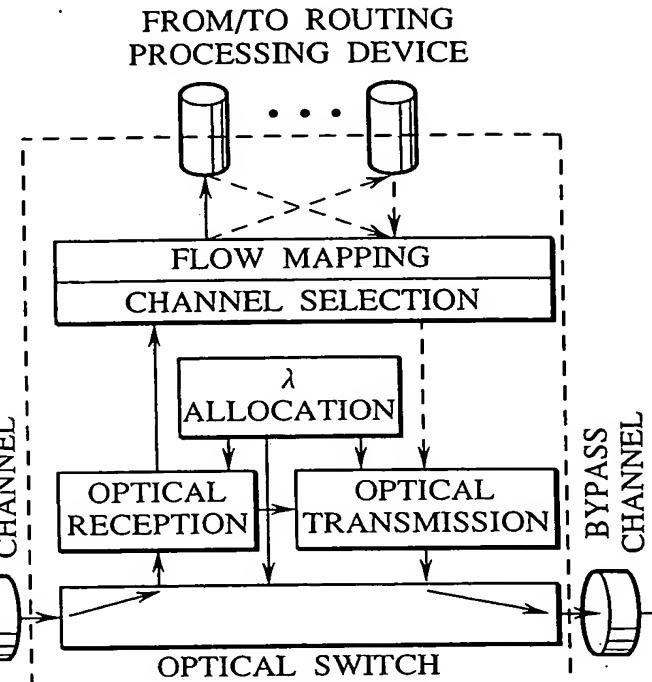


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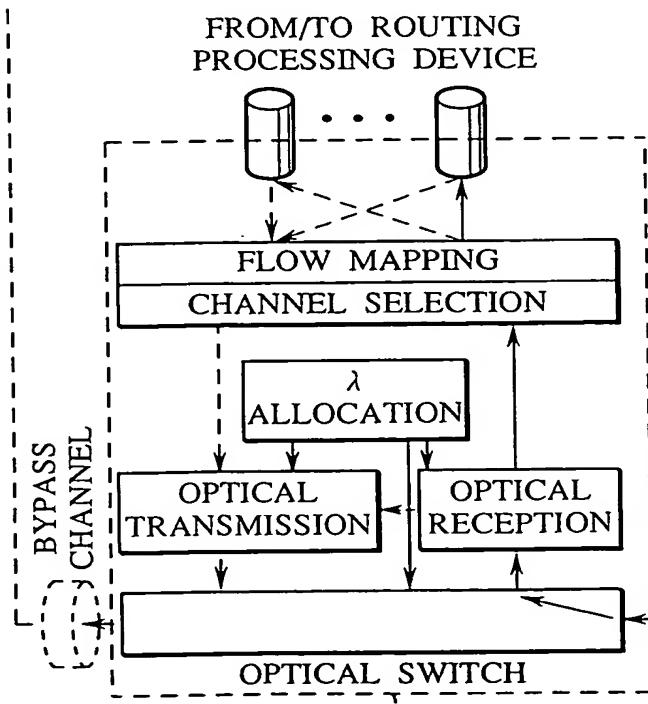
FIG.49



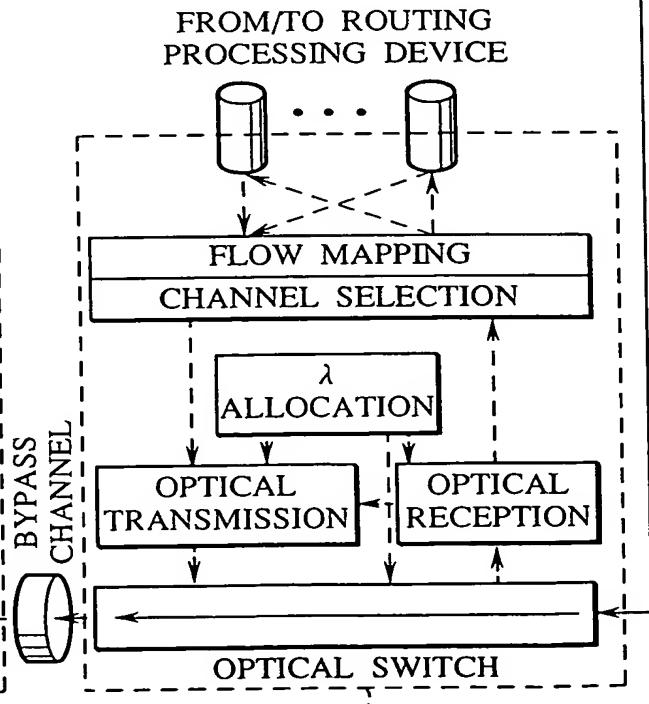
INTERFACE BOARD/CARD 2801-1



INTERFACE BOARD/CARD 2801-2



INTERFACE BOARD/CARD 2801-4



INTERFACE BOARD/CARD 2801-3

FIG.50

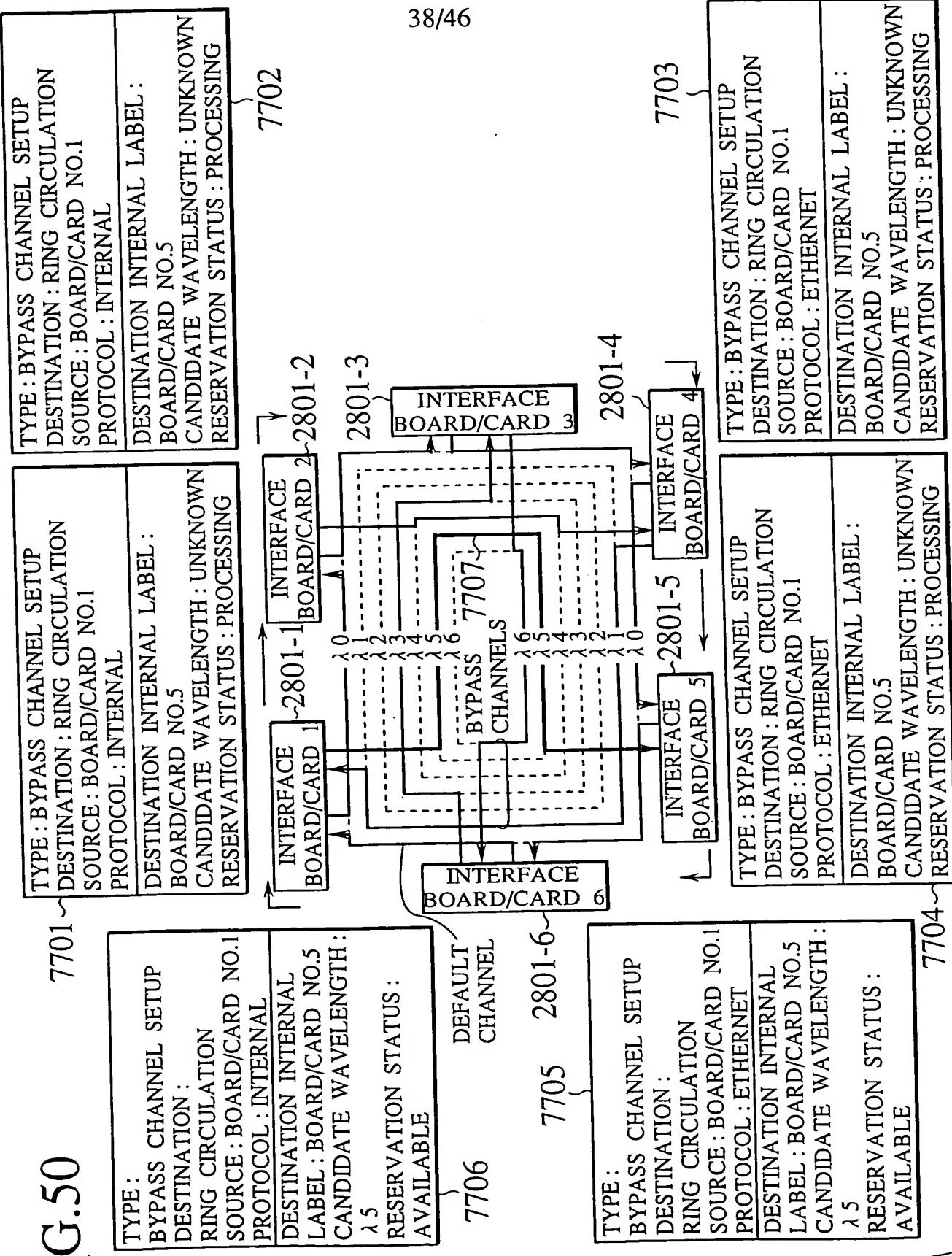
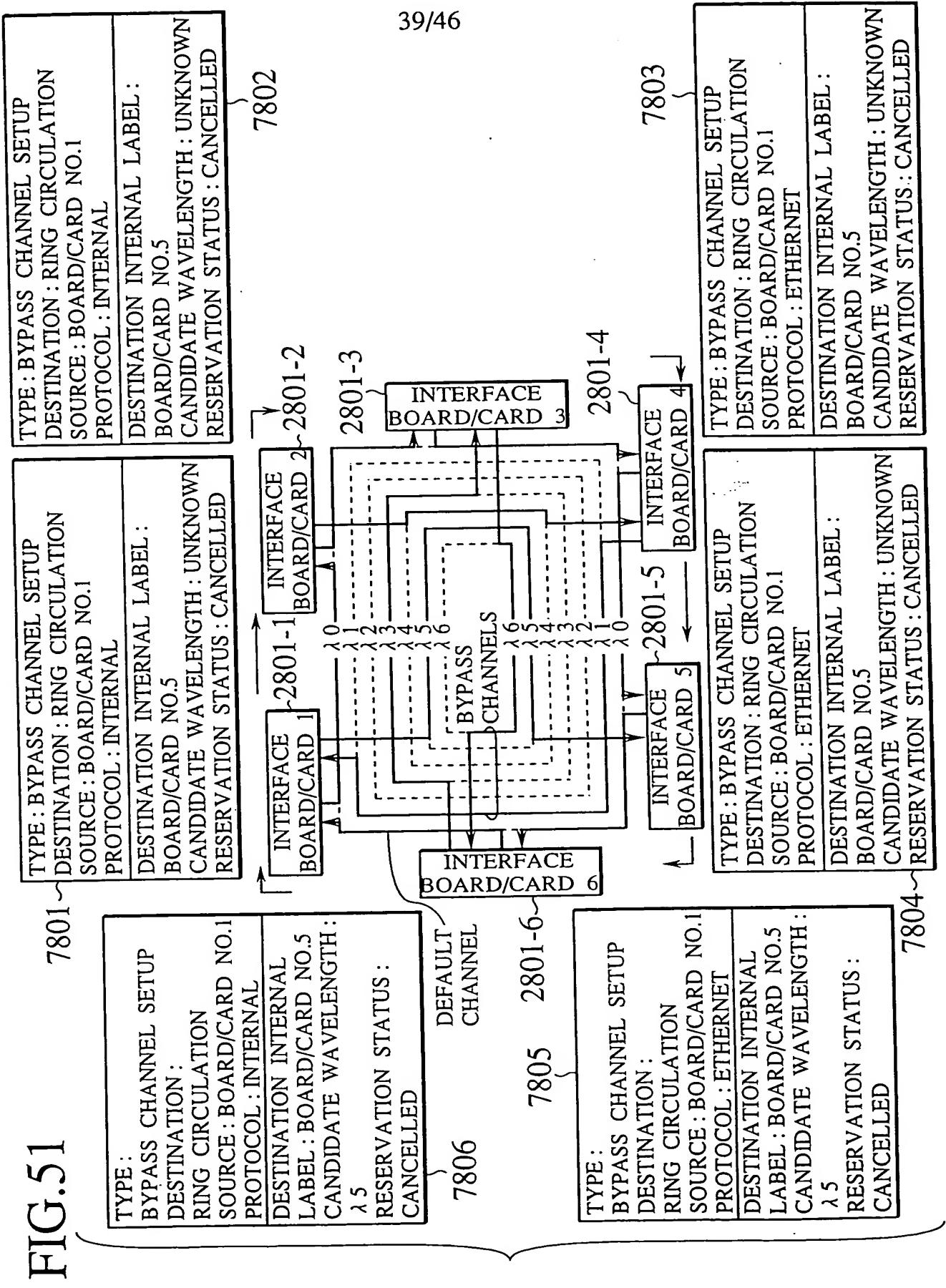


FIG.51



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FIG.52

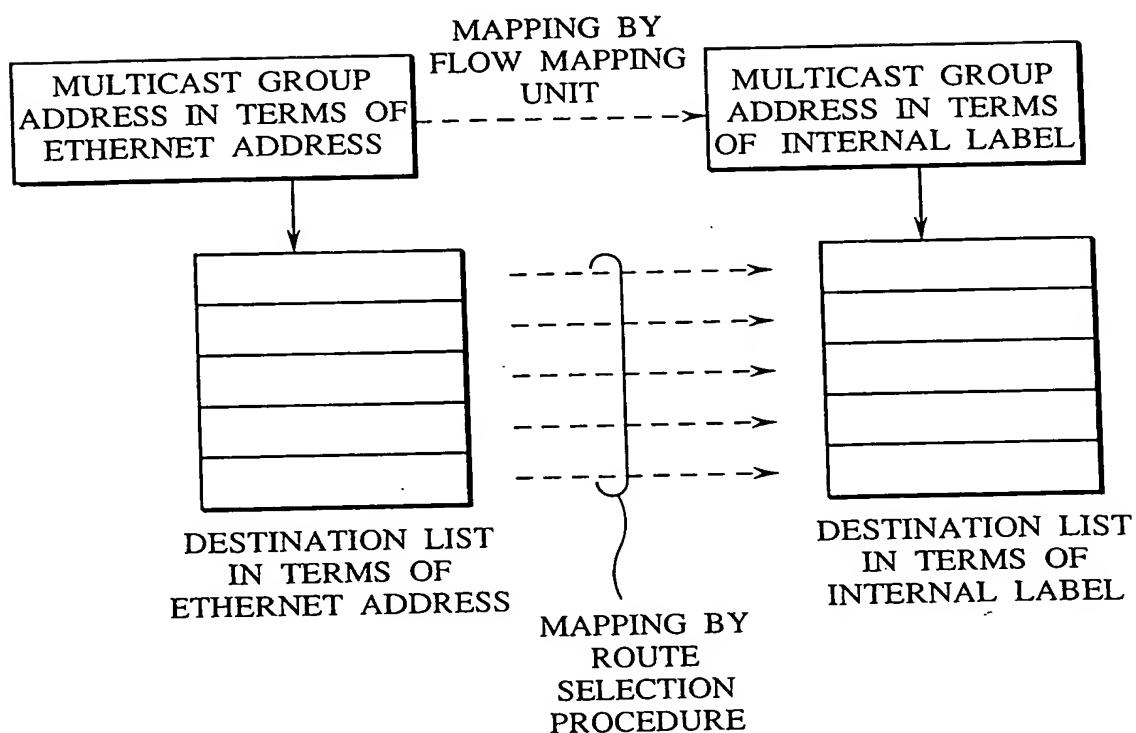


FIG.53

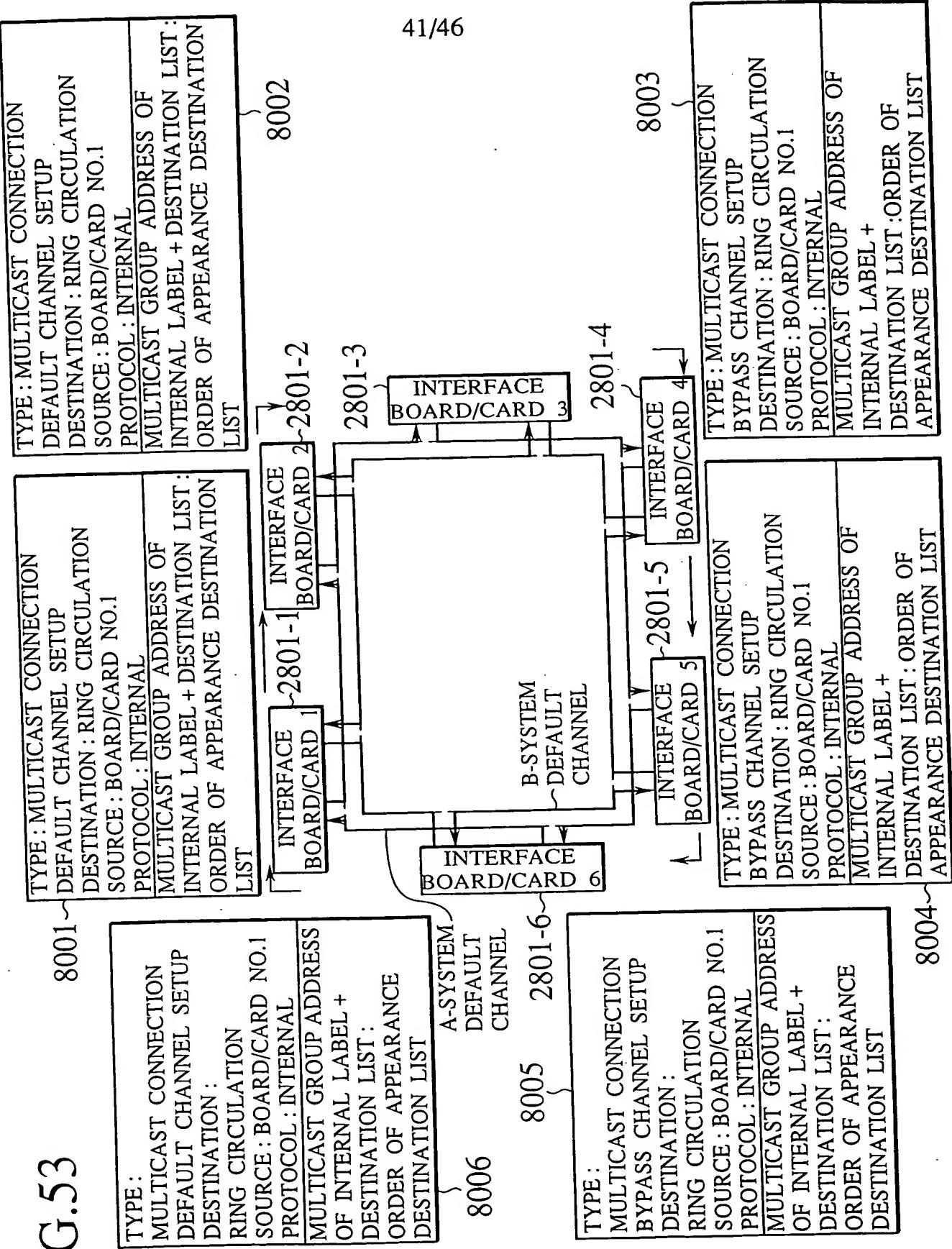


FIG.54

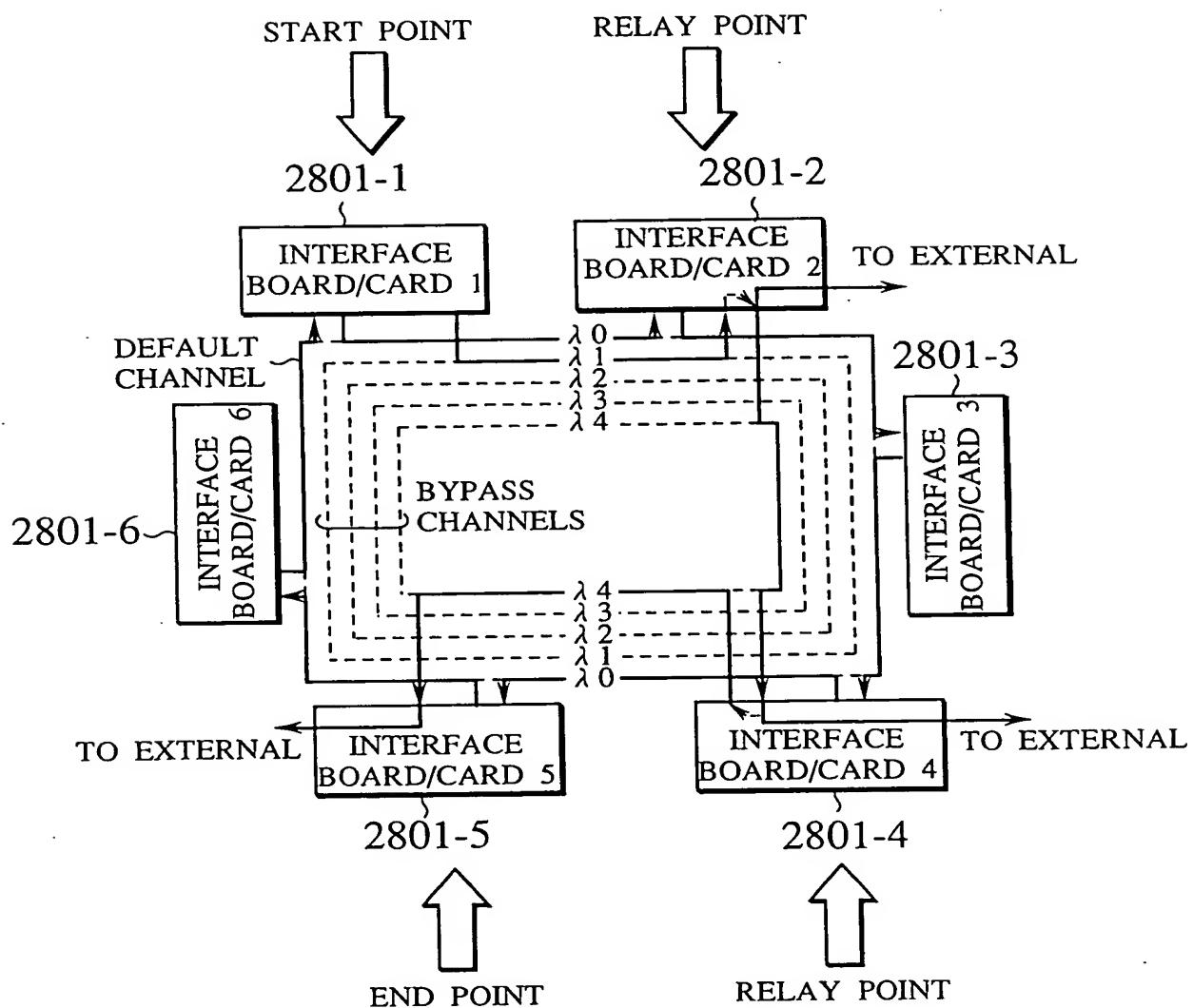


FIG.55

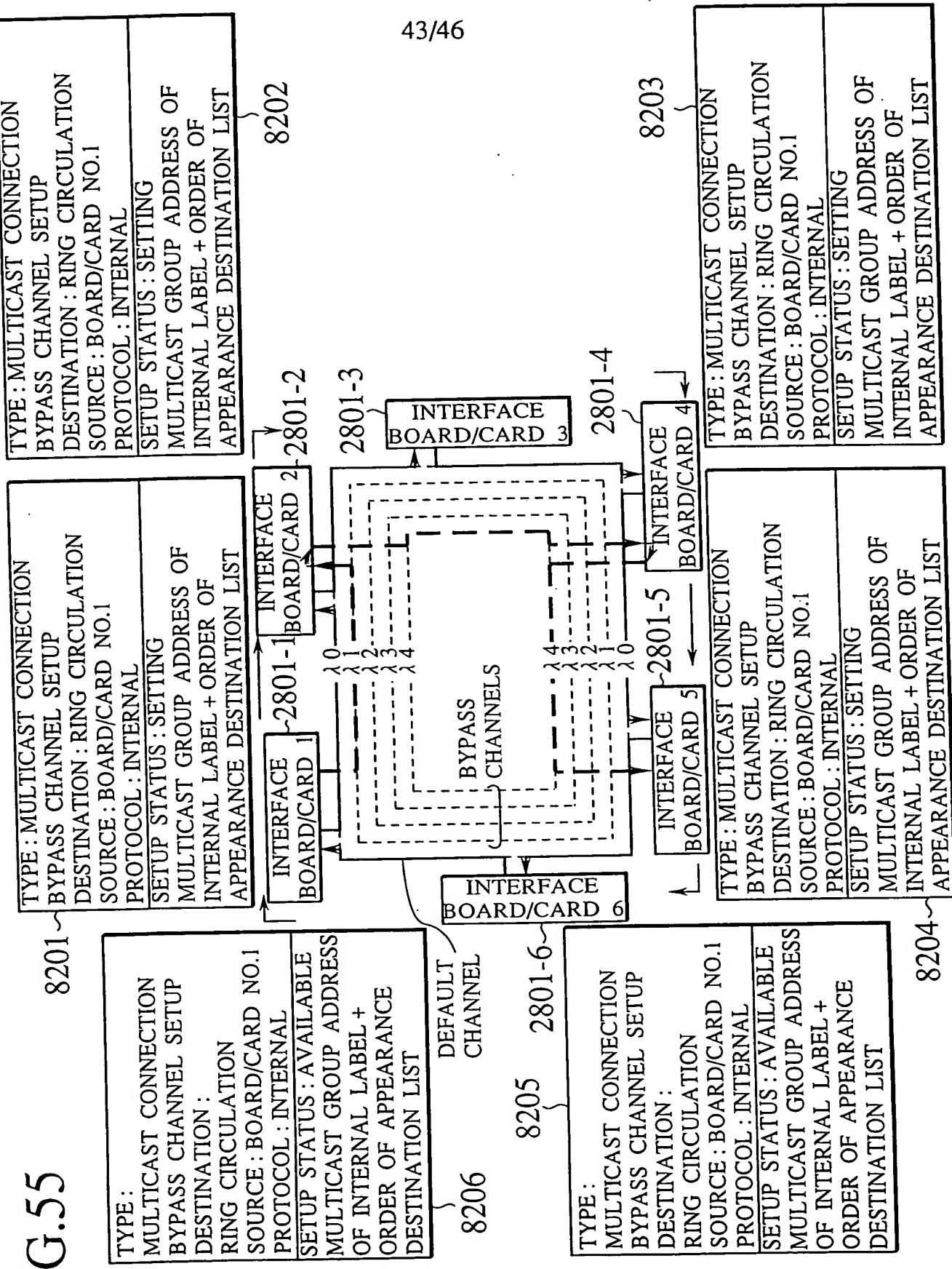


FIG.56

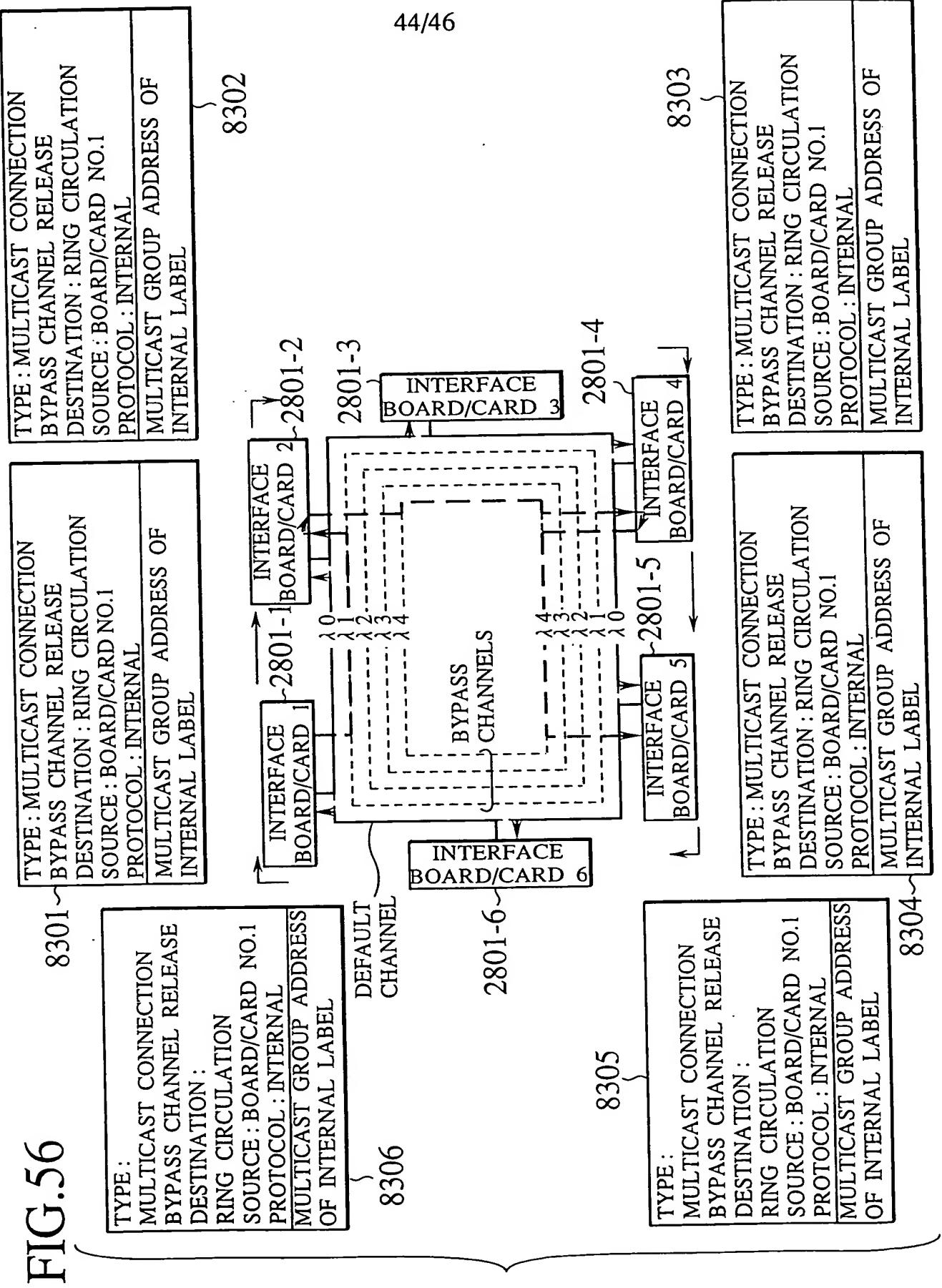


FIG.57
PRIOR ART

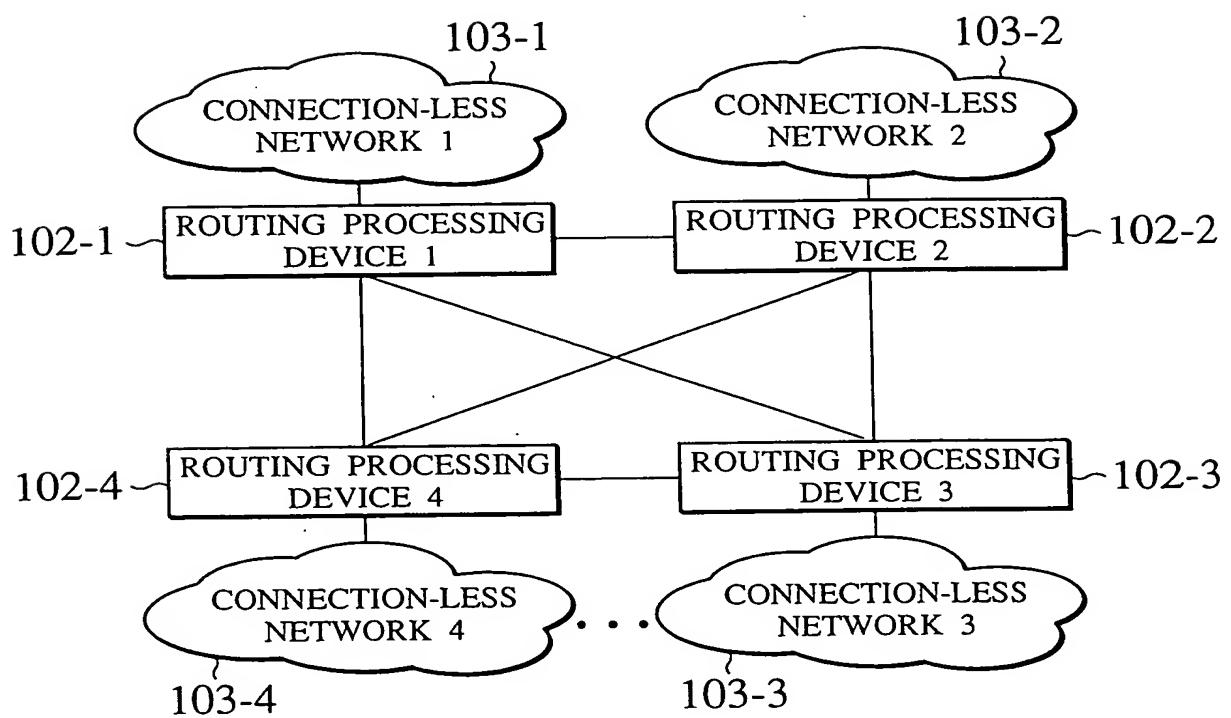


FIG.58

